

# An example of local reasoning in BI pointer logic: the Schorr-Waite graph marking algorithm

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## 1 Introduction

Handling pointers in a programming logic has been one of the most important and difficult problems in programming language research. The difficulty does not lie in coming up with any form of programming logic for pointer programs; in fact, there exist approaches either based on the “pointer as an index of an array” idea [3] or on the use of semantic-based substitutions [4, 1]. Instead, the core of the problem is to obtain a formalism which matches up with an informal reasoning by programmers and algorithm designers. That is, the verification of a program should be only as complicated as an informal correctness argument for the program. Most of the existing formalisms fail in this criteria: when they are used in verifying a pointer program, the verification based on them becomes significantly more complicated than an informal argument.

Only recently, Reynolds, Ishtiaq and O’Hearn [6, 2, 7] pointed out that one way to achieve such a match is to exploit the locality of memory access within a code fragment: usually for a given code fragment, only small number of cells on the heap are accessed. Ishtiaq and O’Hearn formulated a logic for pointer programs based on the logic of Bunched Implications [5] and proposed a rule which captures the locality:

FRAME INTRODUCTION

$$\frac{\{\varphi\} C \{\psi\}}{\{\varphi * \chi\} C \{\psi * \chi\}} \text{ (All stack variables assigned in } C \text{ don't appear in } \chi\text{)}$$

The rationale behind the rule is that if  $\chi$  is a fact on memory cells which are not accessed by  $C$ , the truth of  $\chi$  doesn’t change before and after the execution of  $C$ . The use of  $*$  in  $-\ast\chi$ , the side condition and the “tight” interpretation of a Hoare triple in [2] guarantee that  $\chi$ , in fact, depends only on memory cells which are not accessed by  $C$  at all. With Frame Introduction, we can initially reason about a local property for a piece of code, which amounts to showing a Hoare triple, and then derive a global fact from the proved Hoare triple. O’Hearn calls this style of reasoning *local reasoning*.

The goal of this paper is to show the promise of the idea by one convincing example: the Schorr-Waite graph marking algorithm, which is a well-known test case for any programming logic for pointers because of the complex pointer manipulations in the algorithm. Since the algorithm mixes two different data structures by implementing one data structure inside the other, it is hard to capture its locality of memory access by other known measures in literatures such as reachability

and different field names. The main point of the example is that even for such a program, the locality can still be exploited in the verification with BI pointer logic.

We use the implementation and the specification of the Schorr-Waite graph marking algorithm by Morris and Bornat. They are shown in Section 2. In Section 3, we prove the implementation satisfies the specification. Instead of showing all steps involved, the focus goes on showing the preservation of a loop invariant, which best illustrates the main benefit of BI pointer logic. The verification presented is semi-formal in the sense that we don't give all the detailed semantics of the assertion language and the programming language in this paper, which is slightly different from what is presented in [2].<sup>1</sup> However, we make it clear what is assumed. Throughout the paper, we assume that the reader is familiar with BI pointer logic in [2].

## 2 Marking Algorithm and Specification

The implementation for the Schorr-Waite graph marking algorithm in this section is from [1], which is again Morris' implementation in [4] with minor variation. Since the implementation is verified several times by others, the "semantic" elements of the proof, such as what pre- and post- conditions and loop invariant should mean semantically, are already known. In fact, in this section, these pre- and post- conditions are used to write a specification, and in the next section, the loop invariant is used in the verification. However, the ways to express semantic elements with (classical) BI formulas and to proceed a proof with BI pointer logic are significantly different from existing work. Especially, the proof in the paper shows that locality of memory access can be reflected in the verification. We show the first point in this section, while delaying the more important second one to the next section.

### 2.1 Code

The program below marks all nodes reachable from *root*, which are initially unmarked. It is essentially the depth first traversal with clever implementation of a stack: a stack is implemented using pointer reversals.

The language we are using is slightly different from what appeared in [2]: it is a typed version of that in [2]. Each cell in the heap supposedly has four fields; the first two fields contain only links to other nodes, thereby having *location* type, and the last two fields have only boolean values, thereby having *bool* type. For any cell, the third field of the cell denotes which one of *left* and *right* fields is reversed, and the fourth field indicates whether the cell has been visited or not. In the program, we use *left*, *right*, *check* and *mark* to denote 1st, 2nd, 3rd and 4th fields of a memory cell, respectively. All other stack variables have either *bool* type or *location* type.

```
GRAPH-MARKING(root : location)
local
  t, p, pL, q : location
  pC, tM       : boolean
in
  t := root;
  p := nil;
  if (t <> nil) then (tM := t.mark) else (tM := true);
  while ((p <> nil) or ((t <> nil) and not(tM)))
    do
```

---

<sup>1</sup>The formal semantics is a straightforward modification of that in [2] and will appear in the author's thesis.

```

    if not((t <> nil) and not(tM))
    then pC := p.check;
        if (pC) then q := t;
            t := p;
            p := p.right;
            t.right := q
        else q := t;
            t := p.right;
            pL := p.left;
            p.right := pL;
            p.left := q;
            p.check := true
    else q := p;
        p := t;
        t := t.left;
        p.left := q;
        p.mark := true;
        p.check := false;
    if (t <> nil) then (tM := t.mark) else (tM := true)
end

```

## 2.2 Specification

Any reasonable specification for the Schorr-Waite graph marking algorithm should say that if all nodes reachable from *root* are initially unmarked, after executing `GRAPH-MARKING(root)`, all nodes initially reachable from *root* get marked but only their *mark* and *check* fields are changed. The specification in this section follows this common pattern with slight difference in two aspects: in precondition, we require that all pointers stored in a cell or in the stack variable *root* are not dangling and that all existing cells are reachable from *root*. Even if the second aspect may seem strange to some readers, it reflects the idea of so-called “tight” specification: a specification mentions only those memory cells in concern. In fact, the “full” specification which additionally specifies that all unreachable cells are unchanged follows from the “tight” one by Frame Introduction: if the implementation for the algorithm satisfies the “tight” specification, any properties of unreachable cells can’t change because of Frame Introduction.

We present a specification using a Hoare triple with BI formulas as assertions. The assertion language in this section is slightly different from that in [2] in that it is equipped with sorts (*bool*, *location*, *tree* and two different lists, *blist* and *lblast*); *tree* denotes a finite binary tree whose nodes and leaves contain a value of *location* type, *blist* a finite sequence of boolean values and *lblast* a finite sequence of 4-tuples with three locations and one boolean value. The formal semantics of the sorted assertion language is not presented in the paper.<sup>2</sup> We usually omit the annotation of sorts in assertions.

Before giving a specification for the program, we “define” inductive predicates which state certain properties about finite lists, finite trees and the heap storage. When all cells on the heap are relevant to the truth of a predicate, we add *R* at the end of the name of the predicate to indicate this. The letter *R* stands for “relevant”, and more technically a predicate with *R* at the end of its name indicates that the predicate is not intuitionistic<sup>3</sup>. We use the predicate  $\hookrightarrow$  and the abbreviation

<sup>2</sup>The semantics will appear in the author’s thesis.

<sup>3</sup>A predicate *P* is intuitionistic iff for any *s, h, h'*, *s, h*  $\models$  *P* and *h*  $\sqsubseteq$  *h'* imply that *s, h'*  $\models$  *P*

?. The predicate  $\hookrightarrow$  is an intuitionistic version of the  $\mapsto$  predicate:  $(x \hookrightarrow l, r, c, m)$  is equivalent to  $(x \mapsto l, r, c, m) * \top$ . And ? either in  $\mapsto$  or in  $\hookrightarrow$  stands for an existentially quantified variable. For instance, the formulas  $(x \mapsto ?, ?, c, m)$  and  $(x \hookrightarrow l, r, ?, ?)$  are abbreviations of  $\exists l, r. (x \mapsto l, r, c, m)$  and  $\exists c, m. (x \hookrightarrow l, r, c, m)$ , respectively.

The predicate  $listMarkedNodesR(l, p)$  means that  $p$  denotes the head of a “linked” list of marked nodes which “implements”  $l$ : by following the reversed pointers from  $p$  upto  $nil$ , a finite linked list of marked nodes should be obtained, and each tuple in the finite list  $l$  has to consist of the address and *left*, *right* and *check* fields of the corresponding node in the linked list. As the postfix  $R$  indicates, the predicate also requires that all allocated cells should participate in the implementation. In the specification below, we use the predicate to state that  $p$  denotes the head of a stack, whose content is  $l$ .

$$\begin{aligned} listMarkedNodesR(nil, p) &\equiv (p = nil) \wedge \top \\ listMarkedNodesR((n, l, r, true) :: lbt, p) &\equiv (n = p) \wedge (p \mapsto l, r, true, true) * listMarkedNodesR(lbt, r) \\ listMarkedNodesR((n, l, r, false) :: lbt, p) &\equiv (n = p) \wedge (p \mapsto l, r, false, true) * listMarkedNodesR(lbt, l) \end{aligned}$$

The predicate  $listMarkedNodesR(l, prev)$  is intended to denote that there is a linked list ending with  $prev$  on the heap which represents the “restored” version of the list  $l$ : when either the second or the third value of each tuple in  $l$  is “restored back appropriately” and the list itself is reversed, the first three values of each tuple in the resulting list consists of the address and *left* and *right* fields of the corresponding node in the linked list. And all allocated cells should be a part of the linked list. In the specification below, we use the predicate to denote the heap which is obtained by restoring back the reversed pointers in the stack.

$$\begin{aligned} restoredListR(nil, prev) &\equiv \top \\ restoredListR((n, l, r, true) :: lbt, prev) &\equiv (n \mapsto l, prev, true, true) * restoredListR(lbt, n) \\ restoredListR((n, l, r, false) :: lbt, prev) &\equiv (n \mapsto prev, r, true, true) * restoredListR(lbt, n) \end{aligned}$$

The predicate  $spansR(STree, x)$  denotes that a tree  $STree$  spans all reachable nodes from  $x$  and that moreover all allocated nodes on the heap are reachable from  $x$ .

$$\begin{aligned} spansR(L(n), t) &\equiv (n = t) \wedge \top \\ spansR(N(n, ltree, rtree), t) &\equiv (n = t) \wedge (\exists l, r. (t \mapsto l, r, ?, ?) * spansR(ltree, l) * spansR(rtree, r)) \end{aligned}$$

The predicate  $reach(y, x)$  denotes that there is a path of nodes on the heap from the node  $y$  to the node  $x$ . And  $reachRightChildInList(l, x)$  means that the node  $x$  is reachable from the right child of a node in the list  $l$ .

$$\begin{aligned} reach(y, x) &\equiv \exists path. reachByPathR(y, x, path) * \top \\ reachByPathR(y, x, nil) &\equiv (y = x) \wedge \top \\ reachByPathR(y, x, false :: btl) &\equiv \exists yl, yr. (y \mapsto yl, yr, ?, ?) * reachByPathR(yl, x, btl) \\ reachByPathR(y, x, true :: btl) &\equiv \exists yl, yr. (y \mapsto yl, yr, ?, ?) * reachByPathR(yr, x, btl) \\ reachRightChildInList(nil, x) &\equiv \perp \\ reachRightChildInList((n, l, r, c) :: lbt, x) &\equiv reach(r, x) \vee reachRightChildInList(lt, x) \end{aligned}$$

The predicate  $allocated(x)$  denotes that  $x$  denotes an allocated cell,  $markedR$  that all allocated cells are marked and  $unmarkedR$  that all allocated cells are unmarked.

$$allocated(x) \equiv (x \hookrightarrow ?, ?, ?, ?)$$

$$markedR \equiv \forall x. allocated(x) \rightarrow (x \hookrightarrow ?, ?, ?, true)$$

$$unmarkedR \equiv \forall x. allocated(x) \rightarrow (x \hookrightarrow ?, ?, ?, false)$$

The following two predicates are about dangling pointers;  $noDangling(x)$  says that  $x$  is not a dangling pointer and  $noDanglingR$  that no dangling pointers are stored in memory cells on the heap.

$$noDangling(x) \equiv (x = nil) \vee allocated(x)$$

$$noDanglingR \equiv \forall x, l, r. (x \hookrightarrow l, r, ?, ?) \rightarrow noDangling(l) \wedge noDangling(r)$$

Stating the specification mentioned in the beginning of this section with BI formulas is now straightforward except the way to say that all nodes reachable from  $root$  have the same values for  $left$  and  $right$  fields before and after the execution. We use a spanning tree  $STree$  for the graph of all reachable nodes to store values of  $left$  and  $right$  fields of all reachable nodes.

$$\begin{aligned} &\{spansR(STree, root) \wedge unmarkedR \wedge noDanglingR \wedge noDangling(root)\} \\ &\quad \text{GRAPH-MARKING}(root) \\ &\{spansR(STree, root) \wedge markedR\} \end{aligned}$$

Informally, the specification says that if before executing  $\text{GRAPH-MARKING}(root)$ ,

- all allocated cells are reachable from  $root$  and  $STree$  is a spanning tree of those allocated cells;
- all allocated cells are not marked;
- there are no dangling pointers stored in any cells; and
- $root$  doesn't store a dangling pointer,

then after the execution,

- $STree$  is still a spanning tree of all allocated cells; and
- all allocated cells are marked.

Notice that for two heaps  $h$  and  $h'$ , if they have the same  $STree$  as a spanning tree,  $h$  and  $h'$  have the same allocated cells and the contents of those allocated cells can be different only in the third and fourth fields. Therefore, the mere fact that  $STree$  is a spanning tree before and after the execution implies that no new cells are allocated or deallocated during the execution and that the  $left$  and  $right$  fields of all cells have the same values before and after the execution.

### 3 Verification

In this section, we use BI pointer logic to verify that the implementation of the Schorr-Waite graph marking in Section 2 satisfies the specification. The main focus is on the benefit of local reasoning in

verification. The verification shows that with local reasoning, the verification becomes significantly simpler in the sense that it is only as complicated as an informal argument. Instead of presenting all the details of the proof, only one piece, which best illustrates local reasoning, is shown.

The proof in this section uses three rules which don't come with a particular language construct.<sup>4</sup> The most important one, Frame Introduction, is from [2] and makes it possible to change a local fact for a code fragment into a global fact by incorporating other “unmentioned” cells.

#### FRAME INTRODUCTION

$$\frac{\{\varphi\} C \{\psi\}}{\{\varphi * \chi\} C \{\psi * \chi\}} \text{ (Any assignment in } C \text{ doesn't write to } FV(\chi)\text{)}$$

Auxiliary variables are used frequently in order to state the content of stack variables and the status of the heap before executing a command. And at certain point during the verification, these extra variables should be eliminated from assertions. Auxiliary Elimination is the rule to handle this situation:

#### AUXILIARY ELIMINATION

$$\frac{\{\varphi\} C \{\psi\}}{\{\exists X. \varphi\} C \{\exists X. \psi\}} \text{ (} X \text{ doesn't occur in } C\text{)}$$

The final rule is for slitting states satisfying a precondition into several cases.

#### CASE ANALYSIS

$$\frac{\{\varphi\} C \{\psi\} \quad \{\chi\} C \{\psi\}}{\{\varphi \vee \chi\} C \{\psi\}}$$

---

<sup>4</sup>The soundness of the rules will appear in the author's thesis with the semantics of the BI assertion language in this paper.

### 3.1 Outline

The following code fragment with assertions inserted gives the outline of the proof:

```

{Precondition}
  t := root;
  p := nil;
  { $\exists \text{Stack}. \text{preLoopInvR}(\text{Stack}, p, t, \text{STree}, \text{root})$ }
  if (t <> nil) then (tM := t.mark) else (tM := true);
  { $(\exists \text{Stack}. \text{preLoopInvR}(\text{Stack}, p, t, \text{STree}, \text{root})) \wedge (t = \text{nil} \vee (t \hookrightarrow ?, ?, tM))$ }
  while ((p <> nil) or ((t <> nil) and not(tM)))
  do
    if not((t <> nil) and not(tM))
    then pC := p.check;
      if (pC) then q := t;
        t := p;
        p := p.right;
        t.right := q
      else q := t;
        t := p.right;
        pL := p.left;
        p.right := pL;
        p.left := q;
        p.check := true
    else q := p;
      p := t;
      t := t.left;
      p.left := q;
      p.mark := true;
      p.check := false;
  { $\exists \text{Stack}. \text{preLoopInvR}(\text{Stack}, p, t, \text{STree}, \text{root})$ }
  if (t <> nil) then (tM := t.mark) else (tM := true)
  { $(\exists \text{Stack}. \text{preLoopInvR}(\text{Stack}, p, t, \text{STree}, \text{root})) \wedge (t = \text{nil} \vee (t \hookrightarrow ?, ?, tM))$ }
{Postcondition}

```

where *Precondition* and *Postcondition* are macros for pre- and post- conditions in Section 2:

$$\begin{aligned}
\text{Precondition} &\equiv \text{spansR}(\text{STree}, \text{root}) \wedge \text{unmarkedR} \wedge \text{noDanglingR} \wedge \text{noDangling}(\text{root}) \\
\text{Postcondition} &\equiv \text{spansR}(\text{STree}, \text{root}) \wedge \text{markedR}
\end{aligned}$$

and the predicate *preLoopInvR*, which is the main part of the loop invariant, is defined as follows:

$$\begin{aligned}
&\text{preLoopInvR}(\text{Stack}, P, T, \text{STree}, \text{root}) \\
&\equiv \text{noDanglingR} \wedge \text{noDangling}(T) \wedge \text{noDangling}(P) \\
&\quad \wedge \text{listMarkedNodesR}(\text{Stack}, P) * (\text{restoredListR}(\text{Stack}, T) \multimap \text{spansR}(\text{STree}, \text{root})) \\
&\quad \wedge \text{markedR} * \left( \text{unmarkedR} \right. \\
&\quad \quad \left. \wedge (\forall x. \text{allocated}(x) \rightarrow (\text{reach}(T, x) \vee \text{reachRightChildInList}(\text{Stack}, x))) \right)
\end{aligned}$$

Informally, *preLoopInvR*(*Stack*, *P*, *T*, *STree*, *root*) says that

- *P* and *T* are not dangling, and all allocated cells on the heap don't store dangling pointers;

- $P$  is the head of the stack implemented by pointer reversals,  $Stack$  represents the content of the stack and all nodes in the stack are marked;
- if the reversed links of all nodes in the stack are restored back, where the first reversed link is restored to  $T$ , then all link fields, i.e.,  $left$  and  $right$  fields, of every node on the heap have initial values, which is stored in  $STree$ ; and
- all unmarked allocated nodes are reachable from  $T$  or the  $right$  field of a node stored in the stack ( $Stack$ ) going only through unmarked nodes.

Notice that  $listMarkedNodesR(Stack, P) * (restoredListR(Stack, T) \multimap spansR(STree, root))$  expresses the second and the third points at the same time in a natural way using  $*$  and  $\multimap$ . It states that the heap can be divided into two parts; the first part of the heap implements the stack with  $p$  as its head node; if the stack is taken away from the heap, thereby leaving only the second part, and then the stack with all reversed links restored back is added,  $left$  and  $right$  fields of all allocated nodes have initial values stored in the spanning tree  $STree$ .

Let `init`, `body` and `settM` denote the following code fragments:

```

init ≡ t := root;
      p := nil;

body ≡ if not((t <> nil) and not(tM))
      then pC := p.check;
           if (pC) then q := t;
                    t := p;
                    p := p.right;
                    t.right := q
           else q := t;
                    t := p.right;
                    pL := p.left;
                    p.right := pL;
                    p.left := q;
                    p.check := true
      else q := p;
           p := t;
           t := t.left;
           p.left := q;
           p.mark := true;
           p.check := false;

settM ≡ if (t <> nil) then (tM := t.mark) else (tM := true)

```

Let  $Cond$  denote the condition of the while loop in the program. Then, it suffices to show the following three:

- the loop invariant is established initially;

$$\{Precondition\} \text{init}; \text{settM} \left\{ \begin{array}{l} (\exists Stack. preLoopInvR(Stack, p, t, STree, root)) \\ \wedge (t = nil \vee (t \hookrightarrow ?, ?, tM)) \end{array} \right\}$$



- the loop invariant is preserved by the loop body; and

$$\begin{aligned} & \{(\exists Stack. preLoopInvR(Stack, p, t, STree, root)) \wedge (t = nil \vee (t \hookrightarrow ?, ?, tM)) \wedge Cond\} \\ & \quad \mathbf{body; settM} \\ & \{(\exists Stack. preLoopInvR(Stack, p, t, STree, root)) \wedge (t = nil \vee (t \hookrightarrow ?, ?, tM))\} \end{aligned}$$

- the loop invariant implies the postcondition.

$$\left( \begin{array}{l} (\exists Stack. preLoopInvR(Stack, p, t, STree, root)) \\ \wedge (t = nil \vee (t \hookrightarrow ?, ?, tM)) \\ \wedge \neg(Cond) \end{array} \right) \implies Postcondition$$

By the rule for sequencing, the above three can be derived from the following three Hoare triples and one implication of assertions.

- $\{Precondition\} \mathbf{init} \{ \exists Stack. preLoopInvR(Stack, p, t, STree, root) \}$
- $\left\{ \exists Stack. preLoopInvR(Stack, p, t, STree, root) \right\} \mathbf{settM} \left\{ \begin{array}{l} (\exists Stack. \\ preLoopInvR(Stack, p, t, STree, root)) \\ \wedge (t = nil \vee (t \hookrightarrow ?, ?, tM)) \end{array} \right\}$
- $\left\{ \begin{array}{l} (\exists Stack. \\ preLoopInvR(Stack, p, t, STree, root)) \\ \wedge (t = nil \vee (t \hookrightarrow ?, ?, tM)) \\ \wedge Cond \end{array} \right\} \mathbf{body} \left\{ \exists Stack. preLoopInvR(Stack, p, t, STree, root) \right\}$
- $\left( \begin{array}{l} (\exists Stack. preLoopInvR(Stack, p, t, STree, root)) \\ \wedge (t = nil \vee (t \hookrightarrow ?, ?, tM)) \\ \wedge \neg(Cond) \end{array} \right) \text{ implies } Postcondition.$

Since it is the third case that shows clearly the benefit of local reasoning, we handle only the third one.<sup>5</sup>

### 3.2 Verification of the correctness of body

One common way to informally show the correctness of a program follows three steps: to classify legal input states into certain cases, to compute the program in each case so as to obtain the state change and finally to show that for each case, the changes of memory cells and stack variables, caused by the program, produce legal output states. Before starting the formal verification, we present such an informal correctness argument for the Hoare triple for **body**. During the argument, we point out that all important steps in the informal argument is expressible by implications between assertions.

Informally, the states satisfying the loop invariant and *Cond* can be divided into three cases. The first case is that the current node, denoted by *t*, is either *nil* or marked, the stack is not empty and the right child of the top node, which is pointed to by *p*, in the stack is currently being traversed. The second case is the same same as the first one except that the left child of the top node is being traversed. The third case is the remaining possibility: the current node is neither *nil* nor marked. Since either the *left* field or the *right* field is reversed depending on which child is currently being traversed and all nodes in the stack are marked by the loop invariant, the three cases become more specialized as follows:

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<sup>5</sup>The complete proof will be presented in the author's thesis.

- the current node is either *nil* or marked, the *right* field of the top node is reversed and the top node is marked;

$$(t = \text{nil} \vee (t \hookrightarrow ?, ?, ?, \text{true}) \wedge (tM = \text{true})) \wedge (p \hookrightarrow ?, ?, \text{true}, \text{true})$$

- the current node is either *nil* or marked, the *left* field of the top node is reversed and the top node is marked;

$$(t = \text{nil} \vee (t \hookrightarrow ?, ?, ?, \text{true}) \wedge (tM = \text{true})) \wedge (p \hookrightarrow ?, ?, \text{false}, \text{true})$$

- the current node is neither *nil* nor marked.

$$(t \hookrightarrow ?, ?, ?, \text{false}) \wedge (tM = \text{false})$$

The following lemma states that the informal reasoning, in fact, holds.

**Lemma 1 (Case Splitting)** When all implications in Appendix B.1 hold in the semantics, the assertion

$$(\exists \text{Stack}. \text{preLoopInvR}(\text{Stack}, p, t, \text{STree}, \text{root})) \wedge (t = \text{nil} \vee (t \hookrightarrow ?, ?, ?, tM)) \wedge \text{Cond}$$

implies

$$\begin{aligned} & \left( (t = \text{nil} \vee (t \hookrightarrow ?, ?, ?, \text{true}) \wedge (tM = \text{true})) \wedge (p \hookrightarrow ?, ?, \text{true}, \text{true}) \right) \\ & \vee \left( (t = \text{nil} \vee (t \hookrightarrow ?, ?, ?, \text{true}) \wedge (tM = \text{true})) \wedge (p \hookrightarrow ?, ?, \text{false}, \text{true}) \right) \\ & \vee \left( (t \hookrightarrow ?, ?, ?, \text{false}) \wedge (tM = \text{false}) \right) \end{aligned}$$

**Proof.** The proof is shown in Appendix A.1. □

Each of the three cases takes a different branch in **body**, which makes it easy to figure out the state changes in each case. When  $P$  and  $T$  denote the initial values of  $p$  and  $t$  respectively, **body** changes the store for each case as follows:

- in the first case, the top node in the stack is popped and  $p$  and  $t$  are changed appropriately: the cell  $(P \mapsto PL, PR, \text{true}, \text{true})$  and two stack variables  $p$  and  $t$  are updated to  $(P \mapsto PL, T, \text{true}, \text{true})$ ,  $PR$  and  $P$ , respectively;
- in the second case, the right child of the top node becomes the current node and link fields and check field of the top node are changed appropriately: the cell  $(P \mapsto PL, PR, \text{false}, \text{true})$  and two stack variables  $p$  and  $t$  are updated to  $(P \mapsto T, PL, \text{true}, \text{true})$ ,  $P$  and  $PR$ , respectively; and
- in the third case, the current node is pushed into the stack and stack variables  $p$  and  $t$  are changed appropriately: the cell  $(T \mapsto TL, TR, TC, \text{false})$  and two stack variables  $p$  and  $t$  are updated to  $(T \mapsto P, TR, \text{false}, \text{true})$ ,  $T$  and  $TL$ , respectively.

The last step in an informal reasoning is to argue that for each case, the state change doesn't result in an output state which invalidates the postcondition. For instance, in the first case, it is necessary to show that when the cell  $(P \mapsto PL, PR, \text{true}, \text{true})$  and two stack variables  $p$  and  $t$  are updated to  $(P \mapsto PL, T, \text{true}, \text{true})$ ,  $PR$  and  $P$ , respectively, the resulting state satisfies

the postcondition as long as the input state satisfies the precondition. With BI multiplicative connectives, this informal reasoning is expressible by implications between assertions. The key point is that  $(x \mapsto l, r, c, m) * ((x \mapsto l', r', c', m') \multimap C)$  denotes that if the cell  $(x \mapsto l, r, c, m)$  is updated to  $(x \mapsto l', r', c', m')$ , then  $C$  holds. Recall that in the predicate *preLoopInvR*, the first argument is meant to denote the content of the stack, the second the address of the top node in the stack and the third the address of the current node and that  $p$  and  $t$  are stack variables which keep track of addresses of the top node in the stack and the current node, respectively. The following three lemmas express that for each case, the state change doesn't produce an "illegal" output:

**Lemma 2** When all implications in Appendix B.1 hold in the semantics, the assertion

$$\begin{aligned} & \text{preLoopInvR}((P, PL, PR, \text{true}) :: \text{Stack}, P, T, \text{STree}, \text{root}) \\ & \wedge (T = \text{nil} \vee (T \hookrightarrow ?, ?, \text{true})) \\ & \wedge (P \hookrightarrow PL, PR, \text{true}, \text{true}) \end{aligned}$$

implies

$$\begin{aligned} & (P \mapsto PL, PR, \text{true}, \text{true}) \\ & * ((P \mapsto PL, T, \text{true}, \text{true}) \multimap \text{preLoopInvR}(\text{Stack}, PR, P, \text{STree}, \text{root})) \end{aligned}$$

**Proof.** The proof is shown in Appendix A.2. □

**Lemma 3** When all implications in Appendix B.1 hold in the semantics, the assertion

$$\begin{aligned} & \text{preLoopInvR}((P, PL, PR, \text{false}) :: \text{Stack}, P, T, \text{STree}, \text{root}) \\ & \wedge (T = \text{nil} \vee (T \hookrightarrow ?, ?, \text{true})) \\ & \wedge (P \hookrightarrow PL, PR, \text{false}, \text{true}) \end{aligned}$$

implies

$$\begin{aligned} & (P \mapsto PL, PR, \text{false}, \text{true}) \\ & * ((P \mapsto T, PL, \text{true}, \text{true}) \multimap \text{preLoopInvR}((P, T, PL, \text{true}) :: \text{Stack}, P, PR, \text{STree}, \text{root})) \end{aligned}$$

**Proof.** The proof is shown in Appendix A.3. □

**Lemma 4** When all implications in Appendix B.1 hold in the semantics, the assertion

$$\text{preLoopInvR}(\text{Stack}, P, T, \text{STree}, \text{root}) \wedge (T \hookrightarrow TL, TR, TC, \text{true})$$

implies

$$\begin{aligned} & (T \mapsto TL, TR, TC, \text{true}) \\ & * ((T \mapsto P, TR, \text{false}, \text{true}) \multimap \text{preLoopInvR}((T, P, TR, \text{false}) :: \text{Stack}, T, TL, \text{STree}, \text{root})) \end{aligned}$$

**Proof.** The proof is shown in Appendix A.4. □

In the following sections, we formally prove that if an initial state satisfies the loop invariant and the execution of **body** terminates, the resulting state satisfies the postcondition. Using Lemma 1, the initial states are split into three cases, and we show the following three Hoare triples:

$$\left\{ \begin{array}{l} (\exists \text{Stack}. \\ \text{preLoopInvR}(\text{Stack}, p, t, \text{STree}, \text{root})) \\ \wedge (t = \text{nil} \vee (t \hookrightarrow ?, ?, \text{true}) \wedge (tM = \text{true})) \\ \wedge (p \hookrightarrow ?, ?, \text{true}, \text{true}) \end{array} \right\} \text{body} \left\{ \begin{array}{l} \exists \text{Stack}. \\ \text{preLoopInvR}(\text{Stack}, p, t, \text{STree}, \text{root}) \end{array} \right\}$$

$$\left\{ \begin{array}{l} (\exists \text{Stack}. \\ \text{preLoopInvR}(\text{Stack}, p, t, \text{STree}, \text{root})) \\ \wedge (t = \text{nil} \vee (t \hookrightarrow ?, ?, \text{true}) \wedge tM = \text{true}) \\ \wedge (p \hookrightarrow ?, ?, \text{false}, \text{true}) \end{array} \right\} \text{body} \left\{ \begin{array}{l} \exists \text{Stack}. \\ \text{preLoopInvR}(\text{Stack}, p, t, \text{STree}, \text{root}) \end{array} \right\}$$

$$\left\{ \begin{array}{l} (\exists \text{Stack}. \\ \text{preLoopInvR}(\text{Stack}, p, t, \text{STree}, \text{root})) \\ \wedge (tM = \text{false}) \wedge (t \hookrightarrow ?, ?, \text{false}) \end{array} \right\} \text{body} \left\{ \begin{array}{l} \exists \text{Stack}. \\ \text{preLoopInvR}(\text{Stack}, p, t, \text{STree}, \text{root}) \end{array} \right\}$$

Handling each case is the main part of the paper. For all three cases, the verification has the same format: there are two stages, one for local reasoning and the other for changing a local fact into a global fact. More concretely, in the first stage, we show a certain “local” specification, given by a Hoare triple, holds for **body**, where the specification mentions only those cells accessed by **body**. In the next stage, we use this “local” fact, given by a Hoare triple, to derive another Hoare triple, which is what we originally intended to show and involves all other unaccessed cells in the code fragment. With the three lemmas (Lemma 2, 3 and 4), which are the main part of an informal argument, the change from a local fact to a global fact becomes straightforward, which supports the claim in the paper: with local reasoning, the formal verification becomes only as complicated as an informal one.

### 3.2.1 The first case

The Hoare triple, which corresponds to this case, is the following:

$$\left\{ \begin{array}{l} (\exists \text{Stack}. \\ \text{preLoopInvR}(\text{Stack}, p, t, \text{STree}, \text{root})) \\ \wedge (t = \text{nil} \vee (t \hookrightarrow ?, ?, \text{true}) \wedge (tM = \text{true})) \\ \wedge (\exists L, R. (p \hookrightarrow L, R, \text{true}, \text{true})) \end{array} \right\} \text{body} \left\{ \begin{array}{l} \exists \text{Stack}. \\ \text{preLoopInvR}(\text{Stack}, p, t, \text{STree}, \text{root}) \end{array} \right\}$$

When  $p$  denotes an allocated, checked and marked cell and  $(t = \text{nil} \vee tM = \text{true})$  holds, memory access in **body** shows locality: **body** accesses only the cell initially pointed to by  $p$ . We exploit this locality by first reasoning about a certain local fact for the code fragment and using it to derive the original Hoare triple. The following Hoare triple describes such a local fact:

$$\left\{ \begin{array}{l} (P \mapsto PL, PR, \text{true}, \text{true}) \\ \wedge (t = \text{nil} \vee tM = \text{true}) \\ \wedge (p = P) \wedge (t = T) \end{array} \right\} \text{body} \left\{ \begin{array}{l} (P \mapsto PL, T, \text{true}, \text{true}) \\ \wedge (p = PR) \wedge (t = P) \end{array} \right\}$$

Notice that in any states satisfying the precondition in the above Hoare triple, there should be only one allocated cell, pointed to by  $p$ , on the heap and that the specification describes how

the cell and two stack variables are changed. In a certain sense, the Hoare triple “exactly and only” describes the behavior of `body` when  $p$  denotes an allocated, checked and marked cell and  $(t = \text{nil} \vee tM = \text{true})$  holds. We call the original Hoare triple *global* and the new one in the above *local*. Verifying the local Hoare triple is straightforward as its proof outline below shows:

```

{ (P ↦ PL, PR, true, true) ∧ (t = nil ∨ tM = true) ∧ (p = P) ∧ (t = T) }
if not((t <> nil) and not(tM))
then pC := p.check;
  if (pC) then
    { (P ↦ PL, PR, true, true) ∧ (p = P) ∧ (t = T) }
    q := t;
    t := p;
    p := p.right;
    t.right := q
  else
    { ⊥ }
    q := t;
    t := p.right;
    pL := p.left;
    p.right := pL;
    p.left := q;
    p.check := true
else
  { ⊥ }
  q := p;
  p := t;
  t := t.left;
  p.left := q;
  p.mark := true;
  p.check := false;
{ (P ↦ PL, T, true, true) ∧ (p = PR) ∧ (t = P) }

```

The crucial step of the proof is to change the mode of reasoning from local to global, that is, to derive the global Hoare triple from the local one. It is Frame Introduction and multiplicative connectives  $*$ ,  $\multimap$  of BI that make the change possible, as shown below:

$$\frac{\left\{ \begin{array}{l} (P \mapsto PL, PR, \text{true}, \text{true}) \\ \wedge (t = \text{nil} \vee tM = \text{true}) \\ \wedge (p = P) \wedge (t = T) \end{array} \right\} \text{body} \left\{ \begin{array}{l} (P \mapsto PL, T, \text{true}, \text{true}) \\ \wedge (p = PR) \wedge (t = P) \end{array} \right\}}{\left\{ \begin{array}{l} \left( (P \mapsto PL, PR, \text{true}, \text{true}) \right. \\ \left. \wedge (t = \text{nil} \vee tM = \text{true}) \right. \\ \left. \wedge (p = P) \wedge (t = T) \right) * ((P \mapsto PL, T, \text{true}, \text{true}) \multimap \text{preLoopInvR}(\text{Stack}', PR, P, STree, \text{root})) \end{array} \right\}}$$

body

$$\left\{ \begin{array}{l} (P \mapsto PL, T, \text{true}, \text{true}) \\ \wedge (p = PR) \wedge (t = P) \end{array} \right\} * ((P \mapsto PL, T, \text{true}, \text{true}) \multimap \text{preLoopInvR}(\text{Stack}', PR, P, STree, \text{root}))$$

The last remaining step is to obtain the original global Hoare triple from the above one, which requires to eliminate extra auxiliary variables and to show certain implications between assertions. Auxiliary variables  $P, PL, PR, T$  and  $\text{Stack}'$  can be eliminated easily by applying Auxiliary Elim-

nation:

$$\begin{array}{c}
\vdots \\
\hline
\left\{ \begin{array}{l} \exists P, PL, PR, T, Stack'. \\ \left( \begin{array}{l} (P \mapsto PL, PR, true, true) \\ \wedge (t = nil \vee tM = true) \\ \wedge (p = P) \wedge (t = T) \end{array} \right) * ((P \mapsto PL, T, true, true) \multimap preLoopInvR(Stack', PR, P, STree, root)) \end{array} \right\} \\
\text{body} \\
\left\{ \begin{array}{l} \exists P, PL, PR, T, Stack'. \\ \left( \begin{array}{l} (P \mapsto PL, T, true, true) \\ \wedge (p = PR) \wedge (t = P) \end{array} \right) * ((P \mapsto PL, T, true, true) \multimap preLoopInvR(Stack', PR, P, STree, root)) \end{array} \right\}
\end{array}$$

In order to apply Consequence to obtain the original global Hoare triple from the above one, the two implications between assertions need to be shown: one from the postcondition of the above Hoare triple to that of the global one and the other from the precondition of the global to that of the above. The following sequence of implications shows the first implication. We use certain properties of assertions in Appendix B. The notation  $\vdash$  is used to indicate provability in the BI logic or in the first-order classical logic and  $\models$  to indicate an semantic implication valid in the BI pointer model.

$$\begin{array}{l}
\exists P, PL, PR, T, Stack'. \\
\left( \begin{array}{l} (P \mapsto PL, T, true, true) \\ \wedge (p = PR) \wedge (t = P) \end{array} \right) * ((P \mapsto PL, T, true, true) \multimap preLoopInvR(Stack', PR, P, STree, root))
\end{array}$$

$\implies$  Since  $(p = PR) \wedge (t = P)$  is pure, by Lemma 6 in Appendix B.2,

$$\begin{array}{l}
\exists P, PL, PR, T, Stack'. \\
(P \mapsto PL, T, true, true) * ((P \mapsto PL, T, true, true) \multimap preLoopInvR(Stack', PR, P, STree, root)) \\
\wedge (p = PR) \wedge (t = P)
\end{array}$$

$\implies \varphi * (\varphi \multimap \psi) \vdash \psi$

$$\exists P, PL, PR, T, Stack'. preLoopInvR(Stack', PR, P, STree, root) \wedge (p = PR) \wedge (t = P)$$

$\implies$

$$\exists Stack'. preLoopInvR(Stack', p, t, STree, root)$$

The final step is to show the implication between preconditions, which is shown in the following.

$$\begin{array}{l}
(\exists Stack. preLoopInvR(Stack, p, t, STree, root)) \\
\wedge (\exists PL, PR. (p \hookrightarrow PL, PR, true, true)) \\
\wedge (t = nil \vee (t \mapsto ?, ?, true) \wedge tM = true)
\end{array}$$

$\implies$

$$\begin{array}{l}
\exists Stack, PL, PR. \\
preLoopInvR(Stack, p, t, STree, root) \\
\wedge (p \hookrightarrow PL, PR, true, true) \\
\wedge (t = nil \vee (t \mapsto ?, ?, true) \wedge tM = true)
\end{array}$$

$\implies preLoopInvR(Stack, p, t, STree, root) \vdash restoredListR(Stack, p) * \top$  and Rule 2 in Appendix B.1, which is

$$[ (P \hookrightarrow PL, PR, true, true) \wedge listMarkedNodesR(Stack, P) * \top \models \exists Stack'. Stack = (P, PL, PR, true) :: Stack' ]$$

$$\begin{aligned}
& \exists Stack, PL, PR, P, T, Stack'. \\
& preLoopInvR(Stack, p, t, STree, root)) \\
& \wedge (p \hookrightarrow PL, PR, true, true) \\
& \wedge (t = nil \vee (t \mapsto ?, ?, true) \wedge tM = true) \\
& \wedge (Stack = (PL, PR, true, true) :: Stack') \wedge (p = P) \wedge (t = T)
\end{aligned}$$

$\Rightarrow$

$$\begin{aligned}
& \exists PL, PR, P, T, Stack'. \\
& preLoopInvR((PL, PR, true, true) :: Stack', P, T, STree, root)) \\
& \wedge (P \hookrightarrow PL, PR, true, true) \\
& \wedge (T = nil \vee (T \mapsto ?, ?, true) \wedge tM = true) \\
& \wedge (p = P) \wedge (t = T)
\end{aligned}$$

$\Rightarrow$  Lemma 2

$$\begin{aligned}
& \exists Stack, P, T, PL, PR. \\
& (P \mapsto PL, PR, true, true) * ((P \mapsto PL, T, true, true) \multimap preLoopInvR(Stack', PR, P, STree, root)) \\
& \wedge (T = nil \vee tM = true) \\
& \wedge (p = P) \wedge (t = T)
\end{aligned}$$

$\Rightarrow$  Since  $(t = nil \vee tM = true) \wedge (p = P) \wedge (t = T)$  is pure, by Lemma 6 in Appendix B.2,

$$\begin{aligned}
& \exists PL, PR, P, T, Stack'. \\
& \left( (P \mapsto PL, PR, true, true) \right. \\
& \quad \left. \wedge (t = nil \vee tM = true) \wedge (p = P) \wedge (t = T) \right) * ((P \mapsto PL, T, true, true) \multimap preLoopInvR(Stack', PR, P, STree, root))
\end{aligned}$$

### 3.2.2 The second case

In this case, the following “global” Hoare triple needs to be shown:

$$\left\{ \begin{array}{l} \left( \exists Stack. \right. \\ \left. preLoopInvR(Stack, p, t, STree, root) \right) \\ \wedge (t = nil \vee (t \mapsto ?, ?, true) \wedge tM = true) \\ \left. \wedge (\exists L, R. (p \hookrightarrow L, R, false, true)) \right\} \text{body} \left\{ \exists Stack. \right. \\ \left. preLoopInvR(Stack, p, t, STree, root) \right\}
\end{array} \right.$$

The verification follows the same structure as in Section 3.2.1. First, we prove a “local” Hoare triple which exactly and only describes what **body** does when  $p$  denotes an allocated, unchecked

but marked cell and  $(t = \text{nil} \vee tM = \text{true})$  holds.

```

{ $(P \mapsto PL, PR, \text{false}, \text{true}) \wedge (t = \text{nil} \vee tM = \text{true}) \wedge (p = P) \wedge (t = T)$ }
  if not( $t <> \text{nil}$ ) and not( $tM$ )
  then pC := p.check;
    if (pC) then
      { $\perp$ }
      q := t;
      t := p;
      p := p.right;
      t.right := q
    else
      { $(P \mapsto PL, PR, \text{false}, \text{true}) \wedge (p = P) \wedge (t = T)$ }
      q := t;
      t := p.right;
      pL := p.left;
      p.right := pL;
      p.left := q;
      p.check := true
  else
    { $\perp$ }
    q := p;
    p := t;
    t := t.left;
    p.left := q;
    p.mark := true;
    p.check := false;
    { $(P \mapsto T, PL, \text{true}, \text{true}) \wedge (p = P) \wedge (t = PR)$ }

```

In order to change from the local mode of reasoning to the global mode, we incorporate other portions of the heap by Frame Introduction.

$$\frac{\left\{ \begin{array}{l} (P \mapsto PL, PR, \text{false}, \text{true}) \\ \wedge (t = \text{nil} \vee tM = \text{true}) \\ \wedge (p = P) \wedge (t = T) \end{array} \right\} \text{body } \{(P \mapsto T, PL, \text{true}, \text{true}) \wedge (p = P) \wedge (t = PR)\}}{\left\{ \left( (P \mapsto PL, PR, \text{false}, \text{true}) \wedge (t = \text{nil} \vee tM = \text{true}) \wedge (p = P) \wedge (t = T) \right) * ((P \mapsto T, PL, \text{true}, \text{true}) \text{--} * \text{preLoopInvR}(\text{Stack}', P, PR, \text{STree}, \text{root})) \right\}}$$

body

$$\{(P \mapsto T, PL, \text{true}, \text{true}) \wedge (p = P) \wedge (t = PR)\} * ((P \mapsto T, PL, \text{true}, \text{true}) \text{--} * \text{preLoopInvR}(\text{Stack}', P, PR, \text{STree}, \text{root}))$$

And the auxiliary variables are eliminated by applying Auxiliary Elimination.

$$\frac{\vdots}{\left\{ \exists PL, PR, P, T, \text{Stack}'. \left( (P \mapsto PL, PR, \text{false}, \text{true}) \wedge (t = \text{nil} \vee tM = \text{true}) \wedge (p = P) \wedge (t = T) \right) * ((P \mapsto T, PL, \text{true}, \text{true}) \text{--} * \text{preLoopInvR}(\text{Stack}', P, PR, \text{STree}, \text{root})) \right\}}$$

body

$$\left\{ \exists PL, PR, P, T, \text{Stack}'. \left( (P \mapsto T, PL, \text{true}, \text{true}) \wedge (p = P) \wedge (t = PR) \right) * ((P \mapsto T, PL, \text{true}, \text{true}) \text{--} * \text{preLoopInvR}(\text{Stack}', P, PR, \text{STree}, \text{root})) \right\}$$



The final step is to derive the desired global Hoare triple from the above using Consequence. In the following, we show that the postcondition of the above Hoare triple implies that of the global one.

$$\begin{aligned} & \exists PL, PR, P, T, Stack'. \\ & ((P \mapsto T, PL, true, true) \wedge (p = P) \wedge (t = PR)) * ((P \mapsto T, PL, true, true) \multimap preLoopInvR(Stack', P, PR, STree, root)) \end{aligned}$$

$\Rightarrow$  Since  $(p = P) \wedge (t = PR)$  is pure, by Lemma 6 in Appendix B.2,

$$\begin{aligned} & \exists PL, PR, P, T, Stack'. \\ & (P \mapsto T, PL, true, true) * ((P \mapsto T, PL, true, true) \multimap preLoopInvR(Stack', P, PR, STree, root)) \\ & \wedge (p = P) \wedge (t = PR) \end{aligned}$$

$\Rightarrow \varphi * (\varphi \multimap \psi) \vdash \psi$

$$\exists PL, PR, P, T, Stack'. preLoopInvR(Stack', P, PR, STree, root) \wedge (p = P) \wedge (t = PR)$$

$\Rightarrow$

$$\exists Stack'. preLoopInvR(Stack', p, t, STree, root)$$

The implication between preconditions is shown in the following:

$$\begin{aligned} & (\exists Stack. preLoopInvR(Stack, p, t, STree, root)) \\ & \wedge (\exists PL, PR. (p \hookrightarrow PL, PR, false, true)) \\ & \wedge (t = nil \vee (t \hookrightarrow ?, ?, true) \wedge tM = true) \end{aligned}$$

$\Rightarrow$

$$\begin{aligned} & \exists Stack, PL, PR. \\ & preLoopInvR(Stack, p, t, STree, root) \\ & \wedge (p \hookrightarrow PL, PR, false, true) \\ & \wedge (t = nil \vee (t \hookrightarrow ?, ?, true) \wedge tM = true) \end{aligned}$$

$\Rightarrow preLoopInvR(Stack, p, t, STree, root) \vdash restoredListR(Stack, p) * \top$  and Rule 2 in Appendix B.1, which is

$$[(p \hookrightarrow PL, PR, false, true) \wedge listMarkedNodesR(Stack, p) * \top \models \exists Stack'. Stack = (p, PL, PR, false) :: Stack']$$

$$\begin{aligned} & \exists Stack, PL, PR, Stack', P, T. \\ & preLoopInvR(Stack, p, t, STree, root) \\ & \wedge (p \hookrightarrow PL, PR, false, true) \\ & \wedge (t = nil \vee (t \hookrightarrow ?, ?, true) \wedge tM = true) \\ & \wedge (Stack = (p, PL, PR, false) :: Stack') \wedge (p = P) \wedge (t = T) \end{aligned}$$

$\Rightarrow$

$$\begin{aligned} & \exists PL, PR, Stack', P, T. \\ & preLoopInvR((P, PL, PR, false) :: Stack', P, T, STree, root) \\ & \wedge (P \hookrightarrow PL, PR, false, true) \\ & \wedge (T = nil \vee (T \hookrightarrow ?, ?, true) \wedge tM = true) \wedge (p = P) \wedge (t = T) \end{aligned}$$

$\Rightarrow$  Lemma 3

$$\begin{aligned} & \exists PL, PR, Stack', P, T. \\ & (P \mapsto PL, PR, false, true) * ((P \mapsto T, PL, true, true) \multimap preLoopInvR((P, PL, PR, false) :: Stack', P, T, STree, root)) \\ & \wedge (t = nil \vee tM = true) \wedge (p = P) \wedge (t = T) \end{aligned}$$

$\Rightarrow$  Since  $(t = nil \vee tM = true) \wedge (p = P) \wedge (t = T)$  is pure, by Lemma 6 in Appendix B.2,

$$\begin{aligned} & \exists P, PL, PR, T, Stack'. \\ & \left( (P \mapsto PL, PR, false, true) \right. \\ & \quad \left. \wedge (p = P) \wedge (t = T) \right) * ((P \mapsto T, PL, true, true) \multimap preLoopInvR((P, PL, PR, false) :: Stack', P, T, STree, root)) \\ & \quad \wedge (t = nil \vee tM = false) \end{aligned}$$

### 3.2.3 The third case

The final case is when  $t$  denotes an unmarked cell. The “global” Hoare triple, which needs to be verified in this case, is the following:

$$\left\{ \left( \exists Stack. \right. \right. \\ \left. \left. preLoopInvR(Stack, p, t, STree, root) \right) \right\} \text{body} \left\{ \exists Stack. \right. \\ \left. preLoopInvR(Stack, p, t, STree, root) \right\} \\ \wedge (tM = false) \wedge (t \hookrightarrow ?, ?, false))$$

As in previous two cases, we start the proof of the global Hoare triple with showing that a certain “local” Hoare triple is valid, which precisely describes what **body** does when  $t$  denotes an unmarked allocated cell.

```

{(T ↦ TL, TR, TC, false) ∧ (p = P) ∧ (t = T) ∧ (tM = false)}
  if not((t <> nil) and not(tM))
  then pC := p.check;
      if (pC) then
          {⊥}
          q := t;
          t := p;
          p := p.right;
          t.right := q
      else
          {⊥}
          q := t;
          t := p.right;
          pL := p.left;
          p.right := pL;
          p.left := q;
          p.check := true
  else
      {(T ↦ TL, TR, TC, false) ∧ (p = P) ∧ (t = T)}
      q := p;
      p := t;
      t := t.left;
      p.left := q;
      p.mark := true;
      p.check := false;
      {(T ↦ P, TR, false, true) ∧ (p = T) ∧ (t = TL)}

```

Changing the mode of reasoning from local to global follows the same pattern as in Section 3.2.1 and 3.2.2: we apply Frame Introduction in order to incorporate all other portions of the heap and

and eliminate extra auxiliary variables with Auxiliary Elimination.

$$\frac{\{(T \mapsto TL, TR, TC, false) \wedge (p = P) \wedge (t = T) \wedge (tM = false)\} \text{ body } \{(T \mapsto P, TR, false, true) \wedge (p = T) \wedge (t = TL)\}}{\left\{ \begin{array}{l} \exists P, T, TL, TR, TC, Stack. \\ ((T \mapsto TL, TR, TC, false) \wedge (p = P) \wedge (t = T) \wedge (tM = false)) \\ * ((T \mapsto P, TR, false, true) \text{--} * preLoopInvR((T, P, TR, false) :: Stack, T, TL, STree, root)) \end{array} \right\}} \\ \text{body} \\ \left\{ \begin{array}{l} \exists P, T, TL, TR, Stack. \\ ((T \mapsto P, TR, false, true) \wedge (p = T) \wedge (t = TL)) \\ * ((T \mapsto P, TR, false, true) \text{--} * preLoopInvR((T, P, TR, false) :: Stack, T, TL, STree, root)) \end{array} \right\}$$

It remains to show certain implications between assertions to obtain the global Hoare triple from the above one by Consequence. The following sequence shows the implication from the postcondition in the above Hoare triple to the postcondition of the global one:

$$\begin{aligned} & \exists P, T, TL, TR, Stack. \\ & ((T \mapsto P, TR, false, true) \wedge (p = T) \wedge (t = TL)) \\ & * ((T \mapsto P, TR, false, true) \text{--} * preLoopInvR((T, P, TR, false) :: Stack, T, TL, STree, root)) \\ \Rightarrow & \text{ Since } (p = T) \wedge (t = TL) \text{ is pure, by Lemma 6 in Appendix B.2,} \\ & \exists P, T, TL, TR, Stack. \\ & (T \mapsto P, TR, false, true) * ((T \mapsto P, TR, false, true) \text{--} * preLoopInvR((T, P, TR, false) :: Stack, T, TL, STree, root)) \\ & \wedge (p = T) \wedge (t = TL) \\ \Rightarrow & \varphi * (\varphi \text{--} * \psi) \vdash \psi \\ & \exists P, T, TL, TR, Stack, Stack'. \\ & preLoopInvR((T, P, TR, false) :: Stack, T, TL, STree, root) \wedge (p = T) \wedge (t = TL) \wedge (Stack' = (T, P, TR, false) :: Stack) \\ \Rightarrow & \\ & \exists Stack'. preLoopInvR(Stack', p, t, STree, root) \end{aligned}$$

Finally, the following shows that the precondition in the global Hoare triple implies the precondition in the Hoare triple which we just obtained using Frame Introduction and Auxiliary Elimination:

$$\begin{aligned} & (\exists Stack. preLoopInvR(Stack, p, t, STree, root)) \\ & \wedge (tM = false) \wedge (\exists TL, TR, TC. (t \hookrightarrow TL, TR, TC, false)) \\ \Rightarrow & \\ & \exists Stack, TL, TR, TC. \\ & preLoopInvR(Stack, p, t, STree, root) \\ & \wedge (tM = false) \wedge (t \hookrightarrow TL, TR, TC, false) \\ \Rightarrow & \\ & \exists Stack, TL, TR, TC, T, P. \\ & preLoopInvR(Stack, P, T, STree, root) \\ & \wedge (tM = false) \wedge (T \hookrightarrow TL, TR, TC, false) \\ & \wedge (p = P) \wedge (t = T) \\ \Rightarrow & \text{ Lemma 4} \\ & \exists TL, TR, TC, T, P, Stack. \\ & (T \mapsto TL, TR, TC, false) * ((T \mapsto P, TR, false, true) \text{--} * preLoopInvR((T, P, TR, false) :: Stack, T, TL, STree, root)) \\ & \wedge (tM = false) \wedge (p = P) \wedge (t = T) \\ \Rightarrow & \text{ Since } (tM = false) \wedge (p = P) \wedge (t = T) \text{ is pure, by Lemma 6 in Appendix B.2,} \end{aligned}$$

$\exists TL, TR, TC, T, P, Stack.$   
 $((T \mapsto TL, TR, TC, false) \wedge (p = P) \wedge (t = T))$   
 $* ((T \mapsto P, TR, false, true) \multimap preLoopInvR((T, P, TR, false) :: Stack, T, TL, STree, root))$

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## References

- [1] R. Bornat. Proving pointer programs in Hoare Logic. In *Mathematics of Program Construction*, July 2000.
- [2] S. Ishtiaq and P. O’Hearn. BI as an assertion language for mutable data structures. In *Principles of Programming Languages*, January 2001.
- [3] R. Leino. *Toward reliable modular programs*. PhD thesis, California Institute of Technology, 1995.
- [4] J. M. Morris. A general axiom of assignment. Assignment and linked data structure. A proof of the Schorr-Waite algorithm. In M. Broy and G. Schmidt, editors, *Theoretical Foundations of Programming Methodology (Proceedings of the 1981 Marktoberdorf Summer School)*, pages 25–51. Reidel, 1982.
- [5] P.W. O’Hearn and D.J. Pym. The logic of bunched implications. *Bulletin of Symbolic Logic*, 5(2):215–244, June 99.
- [6] J. C. Reynolds. Intuitionistic reasoning about shared mutable data structure. In *Millennial Perspectives in Computer Science*. Palgrave, 2000.
- [7] J. C. Reynolds. Lectures on reasoning about shared mutable data structure. IFIP Working Group 2.3 School/Seminar on State-of-the-Art Program Design Using Logic, Tandil, Argentina, September 6-13, 2000.

## A Proof of Lemmas

### A.1 Proof of Lemma 1

Since for any state satisfying *Cond* either  $((t = nil) \vee (tM = true)) \wedge (p \neq nil)$  or  $(t \neq nil) \wedge (tM \neq true)$  holds, it suffices to show the following two implications:

$$\begin{aligned}
 & \left( \begin{array}{l} (\exists Stack. preLoopInvR(Stack, p, t, STree, root)) \\ \wedge (t = nil \vee tM = true) \wedge (p \neq nil) \end{array} \right) \implies \left( \begin{array}{l} ((t = nil \vee tM = true) \wedge (p \hookrightarrow ?, ?, true, true)) \\ \vee ((t = nil \vee tM = true) \wedge (p \hookrightarrow ?, ?, false, true)) \end{array} \right) \\
 & \left( \begin{array}{l} (t \neq nil) \wedge (tM \neq true) \\ \wedge (t = nil \vee (t \hookrightarrow ?, ?, ?, tM)) \end{array} \right) \implies \left( (tM = false) \wedge (t \hookrightarrow ?, ?, ?, false) \right)
 \end{aligned}$$

The following sequence of implications proves the first implication.

$$\begin{aligned}
 & ((t = nil) \vee (tM = true)) \wedge (p \neq nil) \wedge (\exists Stack. preLoopInvR(Stack, p, t, STree, root)) \\
 \implies & preLoopInvR(Stack, p, t, STree, root) \vdash noDangling(p) \text{ and } noDangling(p) \equiv (p = nil) \vee (p \hookrightarrow ?, ?, ?, ?) \\
 & ((t = nil) \vee (tM = true)) \wedge (\exists Stack. preLoopInvR(Stack, p, t, STree, root)) \wedge (p \hookrightarrow ?, ?, ?, ?) \\
 \implies & preLoopInvR(Stack, p, t, STree, root) \vdash listMarkedNodesR(Stack, p) * \top \\
 & ((t = nil) \vee (tM = true)) \wedge listMarkedNodesR(Stack, p) * \top \wedge (p \hookrightarrow ?, ?, ?, ?)
 \end{aligned}$$

$$\begin{aligned}
&\Rightarrow \text{Rule 11 in Appendix B.1, which is } \text{listMarkedNodesR}(Lst, p) * \top \wedge (p \hookrightarrow ?, ?, M) \models (M = \text{true}) \\
&\quad ((t = \text{nil}) \vee (tM = \text{true})) \wedge (p \hookrightarrow ?, ?, \text{true}) \\
&\Rightarrow \text{Rule 12 in Appendix B.1, which is } (p \hookrightarrow ?, ?, C, ?) \models (C = \text{true}) \vee (C = \text{false}) \\
&\quad ((t = \text{nil}) \vee (tM = \text{true})) \wedge ((p \hookrightarrow ?, ?, \text{true}, \text{true}) \vee (p \hookrightarrow ?, ?, \text{false}, \text{true})) \\
&\Rightarrow \\
&\quad ((t = \text{nil} \vee tM = \text{true}) \wedge (p \hookrightarrow ?, ?, \text{true}, \text{true})) \vee ((t = \text{nil} \vee tM = \text{true}) \wedge (p \hookrightarrow ?, ?, \text{false}, \text{true}))
\end{aligned}$$

And the following sequence of implications proves the second implication.

$$\begin{aligned}
&(t \neq \text{nil}) \wedge (tM \neq \text{true}) \wedge (t = \text{nil} \vee (t \hookrightarrow ?, ?, tM)) \\
&\Rightarrow \text{Rule 13 in Appendix B.1, which is } \top \models (tM = \text{true}) \vee (tM = \text{false}) \\
&\quad (t \neq \text{nil}) \wedge (tM = \text{false}) \wedge (t = \text{nil} \vee (t \hookrightarrow ?, ?, tM)) \\
&\Rightarrow \\
&\quad (tM = \text{false}) \wedge (t \hookrightarrow ?, ?, \text{false})
\end{aligned}$$

## A.2 Proof of Lemma 2

The following sequence of implications shows the lemma:

$$\begin{aligned}
&\text{preLoopInvR}((P, PL, PR, \text{true}) :: \text{Stack}, P, T, \text{STree}, \text{root}) \\
&\quad \wedge (T = \text{nil} \vee (T \hookrightarrow ?, ?, \text{true})) \\
&\quad \wedge (P \hookrightarrow PL, PR, \text{true}, \text{true}) \\
&\Rightarrow \text{Definition of preLoopInvR} \\
&\quad \text{noDanglingR} \wedge \text{noDangling}(P) \wedge \text{noDangling}(T) \\
&\quad \wedge \text{listMarkedNodesR}((P, PL, PR, \text{true}) :: \text{Stack}, P) * (\text{restoredListR}((P, PL, PR, \text{true}) :: \text{Stack}, T) \multimap \text{spansR}(\text{STree}, \text{root})) \\
&\quad \wedge \text{markedR} * (\text{unmarkedR} \wedge (\forall x. \text{allocated}(x) \rightarrow \text{reach}(T, x) \vee \text{reachRightChildInList}((P, PL, PR, \text{true}) :: \text{Stack}, x))) \\
&\quad \wedge (T = \text{nil} \vee (T \hookrightarrow ?, ?, \text{true})) \\
&\quad \wedge (P \hookrightarrow PL, PR, \text{true}, \text{true}) \\
&\Rightarrow \text{Rule 3 in Appendix B.1, which is } (P \hookrightarrow PL, PR, \text{true}, \text{true}) \wedge \text{noDanglingR} \models \text{noDangling}(PR) \\
&\quad \text{noDanglingR} \wedge \text{noDangling}(T) \wedge \text{noDangling}(PR) \wedge \text{noDangling}(P) \\
&\quad \wedge \text{listMarkedNodesR}((P, PL, PR, \text{true}) :: \text{Stack}, P) * (\text{restoredListR}((P, PL, PR, \text{true}) :: \text{Stack}, T) \multimap \text{spansR}(\text{STree}, \text{root})) \\
&\quad \wedge \text{markedR} * (\text{unmarkedR} \wedge (\forall x. \text{allocated}(x) \rightarrow \text{reach}(T, x) \vee \text{reachRightChildInList}((P, PL, PR, \text{true}) :: \text{Stack}, x))) \\
&\quad \wedge (T = \text{nil} \vee (T \hookrightarrow ?, ?, \text{true})) \\
&\quad \wedge (P \hookrightarrow PL, PR, \text{true}, \text{true}) \\
&\Rightarrow \text{Rules 4,5,6 in Appendix B.1, which are} \\
&\quad \left[ \begin{array}{l} (P \hookrightarrow PL, PR, \text{true}, \text{true}) \wedge \text{noDangling}(T) \wedge \text{noDanglingR} \\ \quad \models (P \mapsto PL, PR, \text{true}, \text{true}) * ((P \mapsto PL, T, \text{true}, \text{true}) \multimap \text{noDanglingR}) \\ (P \hookrightarrow PL, PR, \text{true}, \text{true}) \wedge \text{noDangling}(PR) \\ \quad \models (P \mapsto PL, PR, \text{true}, \text{true}) * ((P \mapsto PL, T, \text{true}, \text{true}) \multimap \text{noDangling}(PR)) \\ (P \hookrightarrow PL, PR, \text{true}, \text{true}) \\ \quad \models (P \mapsto PL, PR, \text{true}, \text{true}) * ((P \mapsto PL, T, \text{true}, \text{true}) \multimap \text{noDangling}(P)) \end{array} \right] \\
&\quad (P \mapsto PL, PR, \text{true}, \text{true}) * ((P \mapsto PL, T, \text{true}, \text{true}) \multimap \text{noDanglingR}) \\
&\quad \wedge (P \mapsto PL, PR, \text{true}, \text{true}) * ((P \mapsto PL, T, \text{true}, \text{true}) \multimap \text{noDangling}(PR)) \\
&\quad \wedge (P \mapsto PL, PR, \text{true}, \text{true}) * ((P \mapsto PL, T, \text{true}, \text{true}) \multimap \text{noDangling}(P)) \\
&\quad \wedge \text{listMarkedNodesR}((P, PL, PR, \text{true}) :: \text{Stack}, P) * (\text{restoredListR}((P, PL, PR, \text{true}) :: \text{Stack}, T) \multimap \text{spansR}(\text{STree}, \text{root})) \\
&\quad \wedge \text{markedR} * (\text{unmarkedR} \wedge (\forall x. \text{allocated}(x) \rightarrow \text{reach}(T, x) \vee \text{reachRightChildInList}((P, PL, PR, \text{true}) :: \text{Stack}, x))) \\
&\quad \wedge (T = \text{nil} \vee (T \hookrightarrow ?, ?, \text{true})) \\
&\Rightarrow \text{Definitions of listMarkedNodesR, restoredListR, reachRightChildInList}
\end{aligned}$$

$$\begin{aligned}
& (P \mapsto PL, PR, true, true) * ((P \mapsto PL, T, true, true) \multimap noDanglingR) \\
& \wedge (P \mapsto PL, PR, true, true) * ((P \mapsto PL, T, true, true) \multimap noDangling(PR)) \\
& \wedge (P \mapsto PL, PR, true, true) * ((P \mapsto PL, T, true, true) \multimap noDangling(P)) \\
& \wedge \left( (P \mapsto PL, PR, true, true) * listMarkedNodesR(Stack, PR) \right. \\
& \quad \left. * ((P \mapsto PL, T, true, true) * restoredListR(Stack, P) \multimap spansR(STree, root)) \right) \\
& \wedge markedR * (unmarkedR \wedge (\forall x. allocated(x) \rightarrow reach(T, x) \vee reach(PR, x) \vee reachRightChildInList(Stack, x))) \\
& \wedge (T = nil \vee (T \hookrightarrow ?, ?, true))
\end{aligned}$$

$$\Rightarrow \varphi * (\psi * \chi \multimap \vartheta) \vdash \psi * (\varphi * (\chi \multimap \vartheta))$$

$$\begin{aligned}
& (P \mapsto PL, PR, true, true) * ((P \mapsto PL, T, true, true) \multimap noDanglingR) \\
& \wedge (P \mapsto PL, PR, true, true) * ((P \mapsto PL, T, true, true) \multimap noDangling(PR)) \\
& \wedge (P \mapsto PL, PR, true, true) * ((P \mapsto PL, T, true, true) \multimap noDangling(P)) \\
& \wedge \left( (P \mapsto PL, PR, true, true) \right. \\
& \quad \left. * \left( (P, PL, T, true, true) \right. \right. \\
& \quad \left. \left. \multimap listMarkedNodesR(Stack, PR) * (restoredListR(Stack, P) \multimap spansR(STree, root)) \right) \right) \\
& \wedge markedR * (unmarkedR \wedge (\forall x. allocated(x) \rightarrow reach(T, x) \vee reach(PR, x) \vee reachRightChildInList(Stack, x))) \\
& \wedge (T = nil \vee (T \hookrightarrow ?, ?, true))
\end{aligned}$$

$\Rightarrow$  Rule 7 in Appendix B.1, which is

$$\left[ \begin{array}{l} ((T = nil) \vee (T \hookrightarrow ?, ?, true)) \\ \wedge markedR * (unmarkedR \wedge (\forall x. allocated(x) \rightarrow reach(T, x) \vee \varphi)) \\ \vdash markedR * (unmarkedR \wedge (\forall x. allocated(x) \rightarrow \varphi)) \end{array} \right]$$

$$\begin{aligned}
& (P \mapsto PL, PR, true, true) * ((P \mapsto PL, T, true, true) \multimap noDanglingR) \\
& \wedge (P \mapsto PL, PR, true, true) * ((P \mapsto PL, T, true, true) \multimap noDangling(PR)) \\
& \wedge (P \mapsto PL, PR, true, true) * ((P \mapsto PL, T, true, true) \multimap noDangling(P)) \\
& \wedge \left( (P \mapsto PL, PR, true, true) \right. \\
& \quad \left. * \left( (P, PL, T, true, true) \right. \right. \\
& \quad \left. \left. \multimap listMarkedNodesR(Stack, PR) * (restoredListR(Stack, P) \multimap spansR(STree, root)) \right) \right) \\
& \wedge markedR * (unmarkedR \wedge (\forall x. allocated(x) \rightarrow reach(PR, x) \vee reachRightChildInList(Stack, x))) \\
& \wedge (T = nil \vee (T \hookrightarrow ?, ?, true))
\end{aligned}$$

$\Rightarrow$  Rule 8 in Appendix B.1, which is

$$\left[ \begin{array}{l} (P \hookrightarrow PL, PR, true, true) \wedge markedR * (unmarkedR \wedge \varphi) \\ \vdash (P \mapsto PL, PR, true, true) * markedR * (unmarkedR \wedge \varphi) \end{array} \right]$$

$$\begin{aligned}
& (P \mapsto PL, PR, true, true) * ((P \mapsto PL, T, true, true) \multimap noDanglingR) \\
& \wedge (P \mapsto PL, PR, true, true) * ((P \mapsto PL, T, true, true) \multimap noDangling(PR)) \\
& \wedge (P \mapsto PL, PR, true, true) * ((P \mapsto PL, T, true, true) \multimap noDangling(P)) \\
& \wedge \left( (P \mapsto PL, PR, true, true) \right. \\
& \quad \left. * \left( (P, PL, T, true, true) \right. \right. \\
& \quad \left. \left. \multimap listMarkedNodesR(Stack, PR) * (restoredListR(Stack, P) \multimap spansR(STree, root)) \right) \right) \\
& \wedge \left( (P \mapsto PL, PR, true, true) * markedR \right. \\
& \quad \left. * (unmarkedR \wedge (\forall x. allocated(x) \rightarrow reach(PR, x) \vee reachRightChildInList(Stack, x))) \right)
\end{aligned}$$

$$\Rightarrow \varphi \vdash \psi * (\psi * \varphi)$$

$$\begin{aligned}
& (P \mapsto PL, PR, true, true) * ((P \mapsto PL, T, true, true) \multimap noDanglingR) \\
& \wedge (P \mapsto PL, PR, true, true) * ((P \mapsto PL, T, true, true) \multimap noDangling(PR)) \\
& \wedge (P \mapsto PL, PR, true, true) * ((P \mapsto PL, T, true, true) \multimap noDangling(P)) \\
& \wedge \left( (P \mapsto PL, PR, true, true) \right. \\
& \quad \left. * \left( (P, PL, T, true, true) \right. \right. \\
& \quad \left. \left. \multimap listMarkedNodesR(Stack, PR) * (restoredListR(Stack, P) \multimap spansR(STree, root)) \right) \right) \\
& \wedge \left( (P \mapsto PL, PR, true, true) * markedR \right. \\
& \quad \left. * \left( (P \mapsto PL, T, true, true) * markedR \right. \right. \\
& \quad \left. \left. * (unmarkedR \wedge (\forall x. allocated(x) \rightarrow reach(PR, x) \vee reachRightChildInList(Stack, x))) \right) \right)
\end{aligned}$$

$\Rightarrow$  Rule 1 in Appendix B.1, which is  $(P \mapsto PL, T, true, true) * markedR \vdash markedR$

$$\begin{aligned}
& (P \mapsto PL, PR, true, true) * ((P \mapsto PL, T, true, true) \multimap noDanglingR) \\
& \wedge (P \mapsto PL, PR, true, true) * ((P \mapsto PL, T, true, true) \multimap noDangling(PR)) \\
& \wedge (P \mapsto PL, PR, true, true) * ((P \mapsto PL, T, true, true) \multimap noDangling(P)) \\
& \wedge \left( (P \mapsto PL, PR, true, true) \right. \\
& \quad \left. * \left( (P, PL, T, true, true) \right. \right. \\
& \quad \quad \left. \left. \multimap listMarkedNodesR(Stack, PR) * (restoredListR(Stack, P) \multimap spansR(STree, root)) \right) \right) \\
& \wedge \left( (P \mapsto PL, PR, true, true) \right. \\
& \quad \left. * \left( (P \mapsto PL, T, true, true) \right. \right. \\
& \quad \quad \left. \left. \multimap markedR * (unmarkedR \wedge (\forall x. allocated(x) \rightarrow reach(PR, x) \vee reachRightChildInList(Stack, x))) \right) \right) \\
\Rightarrow & (P \mapsto PL, PR, true, true) \text{ is strictly exact (Lemma 8 in Appendix B.2) and } (\varphi \multimap \psi) \wedge (\varphi \multimap \chi) \dashv\vdash \varphi \multimap (\psi \wedge \chi)
\end{aligned}$$

$$\begin{aligned}
& (P \mapsto PL, PR, true, true) \\
& \left( (P \mapsto PL, T, true, true) \right. \\
& \quad \left. \multimap \left( \begin{aligned} & noDanglingR \wedge noDangling(PR) \wedge noDangling(P) \\ & \wedge listMarkedNodesR(Stack, PR) * (restoredListR(Stack, P) \multimap spansR(STree, root)) \\ & \wedge markedR * (unmarkedR \wedge (\forall x. allocated(x) \rightarrow reach(PR, x) \vee reachRightChildInList(Stack, x))) \end{aligned} \right) \right) \\
\Rightarrow & \text{Definition of } preLoopInvR
\end{aligned}$$

$$(P \mapsto PL, PR, true, true) * ((P \mapsto PL, T, true, true) \multimap preLoopInvR(Stack, PR, P, STree, root))$$

### A.3 Proof of Lemma 3

The following sequence of implications shows the lemma:

$$\begin{aligned}
& preLoopInvR((P, PL, PR, false) :: Stack, P, T, STree, root) \\
& \wedge (T = nil \vee (T \hookrightarrow ?, ?, true)) \\
& \wedge (P \hookrightarrow PL, PR, false, true)
\end{aligned}$$

$$\Rightarrow \text{Definition of } preLoopInvR$$

$$\begin{aligned}
& noDanglingR \wedge noDangling(P) \wedge noDangling(T) \\
& \wedge listMarkedNodesR((P, PL, PR, false) :: Stack, P) * (restoredListR((P, PL, PR, false) :: Stack, T) \multimap spansR(STree, root)) \\
& \wedge markedR * (unmarkedR \wedge (\forall x. allocatedI(x) \rightarrow reach(T, x) \vee reachRightChildInList((P, PL, PR, false) :: Stack, x))) \\
& \wedge (T = nil \vee (T \hookrightarrow ?, ?, true)) \\
& \wedge (P \hookrightarrow PL, PR, false, true)
\end{aligned}$$

$$\Rightarrow \text{Rule 3 in Appendix B.1, which is } (P \hookrightarrow PL, PR, false, true) \wedge noDanglingR \models noDangling(PR)$$

$$\begin{aligned}
& noDanglingR \wedge noDangling(P) \wedge noDangling(T) \wedge noDanglingR(PR) \\
& \wedge listMarkedNodesR((P, PL, PR, false) :: Stack, P) * (restoredListR((P, PL, PR, false) :: Stack, T) \multimap spansR(STree, root)) \\
& \wedge markedR * (unmarkedR \wedge (\forall x. allocatedI(x) \rightarrow reach(T, x) \vee reachRightChildInList((P, PL, PR, false) :: Stack, x))) \\
& \wedge (T = nil \vee (T \hookrightarrow ?, ?, true))
\end{aligned}$$

$$\Rightarrow \text{Rules 4,5,6 in Appendix B.1, which are}$$

$$\left[ \begin{aligned} & (P \hookrightarrow PL, PR, false, true) \wedge noDanglingR \wedge noDangling(T) \\ & \quad \models (P \mapsto PL, PR, false, true) * ((P \mapsto T, PL, true, true) \multimap noDanglingR) \\ & (P \hookrightarrow PL, PR, false, true) \wedge noDangling(PR) \\ & \quad \models (P \mapsto PL, PR, false, true) * ((P \mapsto T, PL, true, true) \multimap noDangling(PR)) \\ & (P \hookrightarrow PL, PR, false, true) \\ & \quad \models ((P \mapsto T, PL, true, true) \multimap noDangling(P)) \end{aligned} \right]$$

$$\begin{aligned}
& (P \mapsto PL, PR, false, true) * ((P \mapsto T, PL, true, true) \multimap noDanglingR) \\
& \wedge (P \mapsto PL, PR, false, true) * ((P \mapsto T, PL, true, true) \multimap noDangling(P)) \\
& \wedge (P \mapsto PL, PR, false, true) * ((P \mapsto T, PL, true, true) \multimap noDangling(PR)) \\
& \wedge listMarkedNodesR((P, PL, PR, false) :: Stack, P) * (restoredListR((P, PL, PR, false) :: Stack, T) \multimap spansR(STree, root)) \\
& \wedge markedR * (unmarkedR \wedge (\forall x. allocated(x) \rightarrow reach(T, x) \vee reachRightChildInList((P, PL, PR, false) :: Stack, x))) \\
& \wedge (T = nil \vee (T \hookrightarrow ?, ?, true))
\end{aligned}$$

$$\Rightarrow \text{Definitions of } listMarkedNodesR, restoredListR, reachRightChildInList$$

$$\begin{aligned}
& (P \mapsto PL, PR, false, true) * ((P \mapsto T, PL, true, true) \multimap noDanglingR) \\
& \wedge (P \mapsto PL, PR, false, true) * ((P \mapsto T, PL, true, true) \multimap noDangling(P)) \\
& \wedge (P \mapsto PL, PR, false, true) * ((P \mapsto T, PL, true, true) \multimap noDangling(PR)) \\
& \wedge \left( (P \mapsto PL, PR, false, true) * listMarkedNodesR(Stack, PL) \right. \\
& \quad \left. * (restoredListR((P, T, PL, true) :: Stack, PR) \multimap spansR(STree, root)) \right) \\
& \wedge markedR * (unmarkedR \wedge (\forall x. allocatedI(x) \rightarrow reach(T, x) \vee reach(PR, x) \vee reachRightChildInList(Stack, x))) \\
& \wedge (T = nil \vee (T \hookrightarrow ?, ?, true))
\end{aligned}$$

$$\Rightarrow \varphi \vdash \psi * (\psi * \varphi)$$

$$\begin{aligned}
& (P \mapsto PL, PR, false, true) * ((P \mapsto T, PL, true, true) \multimap noDanglingR) \\
& \wedge (P \mapsto PL, PR, false, true) * ((P \mapsto T, PL, true, true) \multimap noDangling(P)) \\
& \wedge (P \mapsto PL, PR, false, true) * ((P \mapsto T, PL, true, true) \multimap noDangling(PR)) \\
& \wedge \left( (P \mapsto PL, PR, false, true) \right. \\
& \quad \left. * \left( (P \mapsto T, PL, true, true) * listMarkedNodesR(Stack, PL) \right. \right. \\
& \quad \quad \left. \left. * (restoredListR((P, T, PL, true) :: Stack, PR) \multimap spansR(STree, root)) \right) \right) \\
& \wedge markedR * (unmarkedR \wedge (\forall x. allocatedI(x) \rightarrow reach(T, x) \vee reach(PR, x) \vee reachRightChildInList(Stack, x))) \\
& \wedge (T = nil \vee (T \hookrightarrow ?, ?, true))
\end{aligned}$$

$$\Rightarrow \text{Rule 7 in Appendix B.1, which is}$$

$$\left[ \begin{array}{l} ((T = nil) \vee (T \hookrightarrow ?, ?, true)) \\ \wedge markedR * (unmarkedR \wedge (\forall x. allocatedI(x) \rightarrow reach(T, x) \vee \varphi)) \\ \vdash \wedge markedR * (unmarkedR \wedge (\forall x. allocatedI(x) \rightarrow \varphi)) \end{array} \right]$$

$$\begin{aligned}
& (P \mapsto PL, PR, false, true) * ((P \mapsto T, PL, true, true) \multimap noDanglingR) \\
& \wedge (P \mapsto PL, PR, false, true) * ((P \mapsto T, PL, true, true) \multimap noDangling(P)) \\
& \wedge (P \mapsto PL, PR, false, true) * ((P \mapsto T, PL, true, true) \multimap noDangling(PR)) \\
& \wedge \left( (P \mapsto PL, PR, false, true) \right. \\
& \quad \left. * \left( (P \mapsto T, PL, true, true) * listMarkedNodesR(Stack, PL) \right. \right. \\
& \quad \quad \left. \left. * (restoredListR((P, T, PL, true) :: Stack, PR) \multimap spansR(STree, root)) \right) \right) \\
& \wedge markedR * (unmarkedR \wedge (\forall x. allocatedI(x) \rightarrow reach(PR, x) \vee reachRightChildInList(Stack, x)))
\end{aligned}$$

$$\Rightarrow \text{Rule 8 in Appendix B.1, which is}$$

$$\left[ \begin{array}{l} (P \hookrightarrow PL, PR, false, true) \wedge markedR * (unmarkedR \wedge \varphi) \\ \vdash (P \hookrightarrow PL, PR, false, true) * markedR * (unmarkedR \wedge \varphi) \end{array} \right]$$

$$\begin{aligned}
& (P \mapsto PL, PR, false, true) * ((P \mapsto T, PL, true, true) \multimap noDanglingR) \\
& \wedge (P \mapsto PL, PR, false, true) * ((P \mapsto T, PL, true, true) \multimap noDangling(P)) \\
& \wedge (P \mapsto PL, PR, false, true) * ((P \mapsto T, PL, true, true) \multimap noDangling(PR)) \\
& \wedge \left( (P \mapsto PL, PR, false, true) \right. \\
& \quad \left. * \left( (P \mapsto T, PL, true, true) * listMarkedNodesR(Stack, PL) \right. \right. \\
& \quad \quad \left. \left. * (restoredListR((P, T, PL, true) :: Stack, PR) \multimap spansR(STree, root)) \right) \right) \\
& \wedge \left( (P \mapsto PL, PR, false, true) * markedR \right. \\
& \quad \left. * (unmarkedR \wedge (\forall x. allocatedI(x) \rightarrow reach(PR, x) \vee reachRightChildInList(Stack, x))) \right)
\end{aligned}$$

$$\Rightarrow \varphi \vdash \psi * (\psi * \varphi)$$

$$\begin{aligned}
& (P \mapsto PL, PR, false, true) * ((P \mapsto T, PL, true, true) \multimap noDanglingR) \\
& \wedge (P \mapsto PL, PR, false, true) * ((P \mapsto T, PL, true, true) \multimap noDangling(P)) \\
& \wedge (P \mapsto PL, PR, false, true) * ((P \mapsto T, PL, true, true) \multimap noDangling(PR)) \\
& \wedge \left( (P \mapsto PL, PR, false, true) \right. \\
& \quad \left. * \left( (P \mapsto T, PL, true, true) * listMarkedNodesR(Stack, PL) \right. \right. \\
& \quad \quad \left. \left. * (restoredListR((P, T, PL, true) :: Stack, PR) \multimap spansR(STree, root)) \right) \right) \\
& \wedge \left( (P \mapsto PL, PR, false, true) \right. \\
& \quad \left. * \left( (P \mapsto T, PL, true, true) * markedR \right. \right. \\
& \quad \quad \left. \left. * (unmarkedR \wedge (\forall x. allocatedI(x) \rightarrow reach(PR, x) \vee reachRightChildInList(Stack, x))) \right) \right)
\end{aligned}$$

$$\Rightarrow \text{Rule 1 in Appendix B.1, which is } (P \mapsto T, PL, true, true) * markedR \vdash markedR$$



$$\begin{aligned}
& (P \mapsto PL, PR, false, true) * ((P \mapsto T, PL, true, true) \multimap noDanglingR) \\
& \wedge (P \mapsto PL, PR, false, true) * ((P \mapsto T, PL, true, true) \multimap noDangling(P)) \\
& \wedge (P \mapsto PL, PR, false, true) * ((P \mapsto T, PL, true, true) \multimap noDangling(PR)) \\
& \wedge \left( (P \mapsto PL, PR, false, true) \right. \\
& \quad * \left( (P \mapsto T, PL, true, true) \right. \\
& \quad \quad * \left( (P \mapsto T, PL, true, true) * listMarkedNodesR(Stack, PL) \right. \\
& \quad \quad \quad * (restoredListR((P, T, PL, true) :: Stack, PR) \multimap spansR(STree, root))) \left. \right) \left. \right) \\
& \wedge \left( (P \mapsto PL, PR, false, true) \right. \\
& \quad * \left( (P \mapsto T, PL, true, true) \right. \\
& \quad \quad * (markedR \\
& \quad \quad \quad * (unmarkedR \wedge (\forall x. allocatedI(x) \rightarrow reach(PR, x) \vee reachRightChildInList(Stack, x))) \left. \right) \left. \right) \left. \right) \\
& \implies (P \mapsto PL, PR, false, true) \text{ is strictly exact (Lemma 8 in Appendix B.2) and } \varphi \multimap (\psi \wedge \chi) \dashv \vdash (\varphi \multimap \psi) \wedge (\varphi \multimap \chi)
\end{aligned}$$

$$\begin{aligned}
& (P \mapsto PL, PR, false, true) \\
& * \left( (P \mapsto T, PL, true, true) \right. \\
& \quad * \left( (P \mapsto T, PL, true, true) * listMarkedNodesR(Stack, PL) \right. \\
& \quad \quad * (restoredListR((P, T, PL, true) :: Stack, PR) \multimap spansR(STree, root))) \left. \right) \\
& \quad * (markedR * (unmarkedR \wedge (\forall x. allocatedI(x) \rightarrow reach(PR, x) \vee reachRightChildInList(Stack, x)))) \left. \right) \\
& \implies \vee\text{-Introduction and Definitions of } listMarkedNodesR \text{ and } reachRightChildInList
\end{aligned}$$

$$\begin{aligned}
& (P \mapsto PL, PR, false, true) \\
& * \left( (P \mapsto T, PL, true, true) \right. \\
& \quad * \left( (P \mapsto T, PL, true, true) * listMarkedNodesR((P, T, PL, true) :: Stack, P) \right. \\
& \quad \quad * (restoredListR((P, T, PL, true) :: Stack, PR) \multimap spansR(STree, root))) \left. \right) \\
& \quad * (markedR * (unmarkedR \wedge (\forall x. allocatedI(x) \rightarrow reach(PR, x) \vee reachRightChildInList((P, T, PL, true) :: Stack, x)))) \left. \right) \\
& \implies \text{Definition of } preLoopInvR
\end{aligned}$$

$$preLoopInvR(Stack, P, PR, STree, root)$$

## A.4 Proof of Lemma 4

The following sequence of implications shows the lemma:

$$preLoopInvR(Stack, P, T, STree, root) \wedge (T \hookrightarrow TL, TR, TC, true)$$

$\implies$  Definition of  $preLoopInvR$

$$\begin{aligned}
& noDanglingR \wedge noDangling(P) \wedge noDangling(T) \\
& \wedge listMarkedNodesR(Stack, P) * (restoredListR(Stack, P) \multimap spansR(STree, root)) \\
& \wedge markedR * (unmarkedR \wedge (\forall x. allocated(x) \rightarrow reach(T, x) \vee reachRightChildInList(Stack, x))) \\
& \wedge (T \hookrightarrow TL, TR, TC, true)
\end{aligned}$$

$\implies$  Rule 1 in Appendix B.1, which is  $(T \hookrightarrow TL, TR, TC, false) \wedge noDanglingR \models noDanglingR(TL)$

$$\begin{aligned}
& noDanglingR \wedge noDangling(P) \wedge noDangling(T) \wedge noDanglingR(TL) \\
& \wedge listMarkedNodesR(Stack, P) * (restoredListR(Stack, P) \multimap spansR(STree, root)) \\
& \wedge markedR * (unmarkedR \wedge (\forall x. allocated(x) \rightarrow reach(T, x) \vee reachRightChildInList(Stack, x))) \\
& \wedge (T \hookrightarrow TL, TR, TC, true)
\end{aligned}$$

$\implies$  Rules 4 and 5 in Appendix B.1, which are

$$\left[ \begin{array}{l}
(T \hookrightarrow TL, TR, TC, false) \wedge noDanglingR \wedge noDangling(P) \\
\quad \models (T \mapsto TL, TR, TC, false) * ((T \mapsto P, TR, false, true) \multimap noDanglingR) \\
(T \hookrightarrow TL, TR, TC, false) \wedge noDanglingR(TL) \\
\quad \models (T \mapsto TL, TR, TC, false) * ((T \mapsto P, TR, false, true) \multimap noDangling(TL)) \\
(T \hookrightarrow TL, TR, TC, false) \\
\quad \models (T \mapsto TL, TR, TC, false) * ((T \mapsto P, TR, false, true) \multimap noDangling(T))
\end{array} \right]$$

$$\begin{aligned}
& (T \mapsto TL, TR, TC, false) * ((T \mapsto P, TR, false, true) \multimap noDanglingR) \\
& \wedge (T \mapsto TL, TR, TC, false) * ((T \mapsto P, TR, false, true) \multimap noDangling(T)) \\
& \wedge (T \mapsto TL, TR, TC, false) * ((T \mapsto P, TR, false, true) \multimap noDangling(TL)) \\
& \wedge listMarkedNodesR(Stack, P) * (restoredListR(Stack, P) \multimap spansR(STree, root)) \\
& \wedge markedR * (unmarkedR \wedge (\forall x. allocated(x) \rightarrow reach(T, x) \vee reachRightChildInList(Stack, x)))
\end{aligned}$$

$\Rightarrow$  Rule 10 in Appendix B.1, which is

$$\left[ \begin{array}{l} (T \mapsto TL, TR, TC, false) \wedge listMarkedNodesR(Stack, P) * (restoredListR(Stack, T) \multimap spansR(STree, root)) \\ \vdash \left( listMarkedNodesR(Stack, P) * (T \mapsto TL, TR, TC, false) \right. \\ \quad \left. * ((T \mapsto TL, TR, true, true) * restoredListR(Stack, T) \multimap spansR(STree, root)) \right) \end{array} \right]$$

$$\begin{aligned}
& (T \mapsto TL, TR, TC, false) * ((T \mapsto P, TR, false, true) \multimap noDanglingR) \\
& \wedge (T \mapsto TL, TR, TC, false) * ((T \mapsto P, TR, false, true) \multimap noDangling(T)) \\
& \wedge (T \mapsto TL, TR, TC, false) * ((T \mapsto P, TR, false, true) \multimap noDangling(TL)) \\
& \wedge \left( (T \mapsto TL, TR, TC, false) * listMarkedNodesR(Stack, P) \right. \\
& \quad \left. * ((T \mapsto TL, TR, true, true) * restoredListR(Stack, T) \multimap spansR(STree, root)) \right) \\
& \wedge markedR * (unmarkedR \wedge (\forall x. allocated(x) \rightarrow reach(T, x) \vee reachRightChildInList(Stack, x)))
\end{aligned}$$

$\Rightarrow$  Rule 9 in Appendix B.1, which is

$$\left[ \begin{array}{l} (T \mapsto TL, TR, TC, false) \wedge markedR * (unmarkedR \wedge (\forall x. allocated(x) \rightarrow reach(T, x) \vee reachRightChildInList(Stack, x))) \\ \vdash \left( markedR * (T \mapsto TL, TR, TC, false) \right. \\ \quad \left( unmarkedR \right. \\ \quad \left. \wedge (\forall x. allocated(x) \rightarrow reach(TL, x) \vee reachI(TR, x) \vee reachRightChildInList(Lst, x)) \right) \end{array} \right]$$

$$\begin{aligned}
& (T \mapsto TL, TR, TC, false) * ((T \mapsto P, TR, false, true) \multimap noDanglingR) \\
& \wedge (T \mapsto TL, TR, TC, false) * ((T \mapsto P, TR, false, true) \multimap noDangling(T)) \\
& \wedge (T \mapsto TL, TR, TC, false) * ((T \mapsto P, TR, false, true) \multimap noDangling(TL)) \\
& \wedge \left( (T \mapsto TL, TR, TC, false) * listMarkedNodesR(Stack, P) \right. \\
& \quad \left. * ((T \mapsto TL, TR, true, true) * restoredListR(Stack, T) \multimap spansR(STree, root)) \right) \\
& \wedge \left( (T \mapsto TL, TR, TC, false) * markedR \right. \\
& \quad \left. * (unmarkedR \wedge (\forall x. allocated(x) \rightarrow reach(TL, x) \vee reach(TR, x) \vee reachRightChildInList(Stack, x))) \right)
\end{aligned}$$

$\Rightarrow \varphi \vdash \psi \multimap (\psi * \varphi)$

$$\begin{aligned}
& (T \mapsto TL, TR, TC, false) * ((T \mapsto P, TR, false, true) \multimap noDanglingR) \\
& \wedge (T \mapsto TL, TR, TC, false) * ((T \mapsto P, TR, false, true) \multimap noDangling(T)) \\
& \wedge (T \mapsto TL, TR, TC, false) * ((T \mapsto P, TR, false, true) \multimap noDangling(TL)) \\
& \wedge \left( (T \mapsto TL, TR, TC, false) \right. \\
& \quad \left. * \left( (T \mapsto P, TR, false, true) \right. \right. \\
& \quad \quad \left. \left. * \left( (T \mapsto P, TR, false, true) * listMarkedNodesR(Stack, P) \right. \right. \right. \\
& \quad \quad \quad \left. \left. * ((T \mapsto TL, TR, true, true) * restoredListR(Stack, T) \multimap spansR(STree, root)) \right) \right) \right) \\
& \wedge \left( (T \mapsto TL, TR, TC, false) \right. \\
& \quad \left. * \left( (T \mapsto P, TR, false, true) \right. \right. \\
& \quad \quad \left. \left. * \left( (T \mapsto TL, TR, TC, false) * markedR \right. \right. \right. \\
& \quad \quad \quad \left. \left. * (unmarkedR \wedge (\forall x. allocated(x) \rightarrow reach(TL, x) \vee reach(TR, x) \vee reachRightChildInList(Stack, x))) \right) \right) \right)
\end{aligned}$$

$\Rightarrow$  Definitions of *listMarkedNodesR*, *restoredListR*, *reachRightChildInList* and Rule 1, which is

$$\left[ (T \mapsto TL, TR, TC, false) * markedR \vdash markedR \right]$$

$$\begin{aligned}
& (T \mapsto TL, TR, TC, false) * ((T \mapsto P, TR, false, true) \multimap noDanglingR) \\
& \wedge (T \mapsto TL, TR, TC, false) * ((T \mapsto P, TR, false, true) \multimap noDangling(T)) \\
& \wedge (T \mapsto TL, TR, TC, false) * ((T \mapsto P, TR, false, true) \multimap noDangling(TL)) \\
& \wedge \left( (T \mapsto TL, TR, TC, false) \right. \\
& \quad \left. * \left( (T \mapsto P, TR, false, true) \right. \right. \\
& \quad \quad \left. \left. * \left( listMarkedNodesR((T, P, TR, false) :: Stack, T) \right. \right. \right. \\
& \quad \quad \quad \left. \left. * (restoredListR((T, P, TR, false) :: Stack, TL) \multimap spansR(STree, root)) \right) \right) \right) \\
& \wedge \left( (T \mapsto TL, TR, TC, false) \right. \\
& \quad \left. * \left( (T \mapsto P, TR, false, true) \right. \right. \\
& \quad \quad \left. \left. * \left( markedR \right. \right. \right. \\
& \quad \quad \quad \left. \left. * (unmarkedR \wedge (\forall x. allocated(x) \rightarrow reach(TL, x) \vee reachRightChildInList((T, P, TR, false) :: Stack, x))) \right) \right) \right)
\end{aligned}$$

$\Rightarrow (T \mapsto TL, TR, TC, false)$  is strictly exact (Lemma 8 in Appendix B.2) and  $\varphi \multimap (\psi \wedge \chi) \dashv\vdash (\varphi \multimap \psi) \wedge (\varphi \multimap \chi)$

$$\begin{aligned}
& (T \mapsto TL, TR, TC, false) \\
& * \left( (T \mapsto P, TR, false, true) \right. \\
& \quad \left. * \left( \begin{aligned} & \wedge \left( \begin{aligned} & noDanglingR \wedge noDangling(TL) \\ & listMarkedNodesR((T, P, TR, false) :: Stack, T) \\ & * (restoredListR((T, P, TR, false) :: Stack, TL) * spansR(STree, root)) \end{aligned} \right) \\ & \wedge \left( \begin{aligned} & markedR \\ & * (unmarkedR \wedge (\forall x. allocated(x) \rightarrow reach(TL, x) \vee reachRightChildInList((T, P, TR, false) :: Stack, x))) \end{aligned} \right) \end{aligned} \right) \right) \\
& \Rightarrow \text{Definition of } preLoopInvR
\end{aligned}$$

$$preLoopInvR((T, P, TR, false) :: Stack, T, TL, STree, root)$$

## B Properties of Assertions

### B.1 Extra Rules

1.  $(x \mapsto l, r, c, true) * markedR \models markedR$
2.  $(x \hookrightarrow l, r, c, true) \wedge listMarkedNodesR(lst, x) * \top \models \exists lst'. lst = (x, l, r, c) :: lst'$
3.  $(x \hookrightarrow l, r, c, m) \wedge noDanglingR \models noDangling(l) \wedge noDangling(r)$
4.  $(x \hookrightarrow l, r, c, m) \wedge noDangling(y) \wedge noDanglingR \models (x \mapsto l, r, c, m) * ((x \mapsto l, y, c, m) \vee (x \mapsto y, r, c, m) * noDanglingR)$
5.  $(x \hookrightarrow l, r, c, m) \wedge noDangling(y) \models (x \mapsto l, r, c, m) * ((x \mapsto l', r', c', m') * noDangling(y))$
6.  $(x \hookrightarrow l, r, c, m) \models (x \mapsto l, r, c, m) * ((x \mapsto l', r', c', m') * noDangling(x))$
7.  $((x = nil) \vee (x \hookrightarrow ?, ?, true)) \wedge markedR * (unmarkedR \wedge (\forall y. allocated(y) \rightarrow reach(x, y) \vee \varphi)) \models markedR * (unmarkedR \wedge (\forall y. allocated(y) \rightarrow \varphi))$
8.  $(x \hookrightarrow l, r, c, true) \wedge markedR * (unmarkedR \wedge \varphi) \models (x \mapsto l, r, c, true) * markedR * (unmarkedR \wedge \varphi)$
9.  $(x \hookrightarrow l, r, c, false) \wedge markedR * (unmarkedR \wedge (\forall y. allocated(y) \rightarrow reach(x, y) \vee reachRightChildInList(lst, y))) \models markedR * (x \mapsto l, r, c, false) * \left( \begin{aligned} & unmarkedR \\ & \wedge (\forall y. allocated(y) \rightarrow reach(l, y) \vee reachI(r, y) \vee reachRightChildInList(lst, y)) \end{aligned} \right)$
10.  $(x \hookrightarrow l, r, c, false) \wedge listMarkedNodesR(lst, y) * (restoredListR(lst, x) * spansR(stree, root)) \models listMarkedNodesR(lst, y) * (x \mapsto l, r, c, false) * ((x \mapsto l, r, true, true) * restoredListR(lst, x) * spansR(stree, root))$
11.  $listMarkedNodesR(lst, x) * \top \wedge (x \hookrightarrow l, r, c, m) \models m = true$
12.  $(x \hookrightarrow l, r, c, m) \models (c = true \vee c = false) \wedge (m = true \vee m = false)$
13. for any stack variable  $x$  of *bool* type,  $\top \models (x = true) \vee (x = false)$

### B.2 Special BI Formulas

**Definition 5 (Pure BI Formula)** A BI formula  $\varphi$  is *pure* iff for any stack  $s$ , the following two are equivalent:

- there exists a heap  $h$  such that  $s, h \models \varphi$ ; and
- for all heaps  $h, s, h \models \varphi$ .

**Lemma 6** For any BI pure formula  $\varphi$  and all BI formulas  $\psi, \chi$ ,

$$(\psi * \chi) \wedge \varphi \models (\psi \wedge \varphi) * \chi \text{ and } (\psi \wedge \varphi) * \chi \models (\psi * \chi) \wedge \varphi$$

**Definition 7 (Strictly Exact BI Formula)** A BI formula  $\varphi$  is *strictly exact* iff for any stack  $s$ , there exists at most one heap  $h$  such that  $s, h \models \varphi$ .

**Lemma 8** For any BI strictly exact formula  $\varphi$ , and all BI formulas  $\psi, \chi$ ,

$$(\varphi * \psi) \wedge (\varphi * \chi) \models \varphi * (\psi \wedge \chi)$$