

Logiweb codex of clueless

Up Help

clueless, Weakening, ConditionedMP, PermuteAntecedents, ImplyTransitivity, EqualReflexivity, EqualSymmetry, EqualTransitivity, Mendelson32d, substantiation, Mendelson32fBase, Mendelson32fInduction, Mendelson32f, Mendelson32gBase, Mendelson32gInduction, Mendelson32g, Switch, AddOne, Mendelson32dRule, Mendelson32dConditionedRule, EqualTransitivityRule, EqualTransitivityConditionedRule, PlusCommutativityBase, PlusCommutativityInduction, PlusCommutativity, DoubleMP, InductionMacro, EqualSymmetryRule, DoubleConditionedMP,

clueless

[clueless $\xrightarrow{\text{pyk}}$ “clueless”]

Weakening

[Weakening $\xrightarrow{\text{tex}}$ “Weakening”]

ConditionedMP

[ConditionedMP $\xrightarrow{\text{tex}}$ “Conditioned MP”]

PermuteAntecedents

[PermuteAntecedents $\xrightarrow{\text{tex}}$ “PermuteAntecedents”]

ImplyTransitivity

[ImplyTransitivity $\xrightarrow{\text{tex}}$ “ImplyTransitivity”]

EqualReflexivity

$[\text{EqualReflexivity} \stackrel{\text{tex}}{\rightarrow} \text{“EqualReflexivity”}]$

EqualSymmetry

$[\text{EqualSymmetry} \stackrel{\text{tex}}{\rightarrow} \text{“EqualSymmetry”}]$

EqualTransitivity

$[\text{EqualTransitivity} \stackrel{\text{tex}}{\rightarrow} \text{“EqualTransitivity”}]$

Mendelson32d

$[\text{Mendelson32d} \stackrel{\text{tex}}{\rightarrow} \text{“Mendelson32d”}]$

substantiation

$[\text{substantiation} \stackrel{\text{tex}}{\rightarrow} \text{“substantiation”}]$

Mendelson32fBase

$[\text{Mendelson32fBase} \stackrel{\text{tex}}{\rightarrow} \text{“Mendelson32fBase”}]$

Mendelson32fInduction

$[\text{Mendelson32fInduction} \stackrel{\text{tex}}{\rightarrow} \text{“Mendelson32fInduction”}]$

Mendelson32f

$[\text{Mendelson32f} \stackrel{\text{tex}}{\rightarrow} \text{“Mendelson32f”}]$

Mendelson32gBase

$[\text{Mendelson32gBase} \stackrel{\text{tex}}{\rightarrow} \text{“Mendelson32gBase”}]$

Mendelson32gInduction

[Mendelson32gInduction $\xrightarrow{\text{tex}}$ “Mendelson32gInduction”]

Mendelson32g

[Mendelson32g $\xrightarrow{\text{tex}}$ “Mendelson32g”]

Switch

[Switch $\xrightarrow{\text{tex}}$ “Switch”]

AddOne

[AddOne $\xrightarrow{\text{tex}}$ “AddOne”]

Mendelson32dRule

[Mendelson32dRule $\xrightarrow{\text{tex}}$ “Mendelson32dRule”]

Mendelson32dConditionedRule

[Mendelson32dConditionedRule $\xrightarrow{\text{tex}}$ “Mendelson32dConditionedRule”]

EqualTransitivityRule

[EqualTransitivityRule $\xrightarrow{\text{tex}}$ “EqualTransitivityRule”]

EqualTransitivityConditionedRule

[EqualTransitivityConditionedRule $\xrightarrow{\text{tex}}$ “EqualTransitivityConditionedRule”]

PlusCommutativityBase

[PlusCommutativityBase $\xrightarrow{\text{tex}}$ “PlusCommutativityBase”]

PlusCommutativityInduction

[PlusCommutativityInduction $\xrightarrow{\text{tex}}$ “PlusCommutativityInduction”]

PlusCommutativity

[PlusCommutativity $\xrightarrow{\text{tex}}$ “PlusCommutativity”]

DoubleMP

[DoubleMP $\xrightarrow{\text{tex}}$ “DoubleMP”]

InductionMacro

[InductionMacro $\xrightarrow{\text{tex}}$ “InductionMacro”]

EqualSymmetryRule

[EqualSymmetryRule $\xrightarrow{\text{tex}}$ “EqualSymmetryRule”]

DoubleConditionedMP

[DoubleConditionedMP $\xrightarrow{\text{tex}}$ “DoubleConditionedMP”]

The pyk compiler, version 0.grue.20050603 by Klaus Grue
GRD-2005-06-21.UTC:18:57:23.412938 = MJD-53542.TAI:18:57:55.412938 =
LGT-4626097075412938e-6